

# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

## Display of VISION, MISSION, PO's, CO's, PEO's, PSO's and Bloom's Taxonomy in Campus

**SREE CHAITANYA**  
INSTITUTE OF TECHNOLOGICAL SCIENCES  
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)

**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**VISION OF THE INSTITUTE**

To emerge as an Institution of Excellence to educate, empower students in the fields of Engineering, Management to create solutions that improve the quality of life and sustainability.

**MISSION OF THE INSTITUTE**

- To deliver high quality Engineering and Management Education by giving prominence to theoretical, practical, experiential and comprehensive learning-teaching with the help of cutting-edge technologies and best in class infrastructure.
- To establish an environment that produce advance knowledge through research and enrich a culture of inquiry, critical thinking, creativity and innovation to address the needs, challenges and issues of the society with best solutions and to ensure its well-being.
- To develop fruitful industry-institution interactive collaborations, curriculums and programs, leading to enhance the competency, leadership and entrepreneurship skill.
- To develop highly professionals through holistic education by focusing on individual growth, discipline, integrity, ethics, compassion, accountability, sustainability and social sensitivity.

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

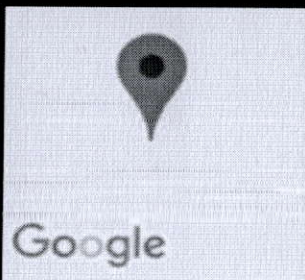
**VISION OF THE DEPARTMENT**

To offer Top-Notch learning in Electrical and Electronics Engineering Education with strong emphasis of providing environment to acquire skills useful for practical applications, innovations and design to meet industrial demands.

**MISSION OF THE DEPARTMENT**

- To establish the foundation for influencing the directions of electrical and electronics engineering in the future.
- To serve as a hub for research and innovation that produces technologies and knowledge.
- Applying in-depth understanding of EEE to generate, expertise and cultivating such expertise for industry and society's use.
- To harness the potential of human creativity, technology and passion for the advancement of humanity and India.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527



SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES, Telangana, India

9503+CVJ, LMD Dam Inspection Rd, LMD Colony, Thimmapur, Panthul Kondapur, Telangana 505527, India

Long 79.154643°

Lat 18.371090°

14/5/2022 01:47 PM

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*Principal*  
Principal

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

SREE CHAITANYA  
INSTITUTE OF TECHNOLOGICAL SCIENCES  
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

### PROGRAM OUTCOMES (PO'S)

After Completion Of The Course Engineering Graduate Will Be Able To:

- PO1: Engineering Knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- PO2: Problem Analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
- PO3: Design/Development of Solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
- PO4: Conduct Investigations of Complex Problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
- PO5: Modern Tool Usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
- PO6: The Engineer and Society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
- PO7: Environment and Sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
- PO8: Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
- PO9: Individual and teamwork:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
- PO10: Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
- PO11: Project Management and Finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply the set one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
- PO12: Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

GPS Map Camera

SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES, Telangana, India

95C3+CVJ, LMD Dam Inspection Rd, LMD Colony, Thimmapur, Panthul Kondapur,  
Telangana 505527, India

Long 79.154643°

Lat 18.371090°

14/5/2022 01:48 PM

Google

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

Principal

Sree Chaitanya Institute of Technological Sciences  
LMD Colony, KARIMNAGAR, T.S.





**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**Program Educational Objectives (PEOs)**

S.No.	PEO Name	Program Educational Objective Statements
1	PEO - 1	To educate students in fundamental sciences and electrical and electronics engineering so that they can meet the issues facing the power industry globally.
2	PEO - 2	To get ready to become a flexible technical engineer who can benefit society in a variety of ways.
3	PEO - 3	To become ready as a capable professional engineer who can solve problems in the real world by working alone, in a team, or as a leader.
4	PEO - 4	To provide lifelong learners with critical thinking, problem solving and reasoning abilities.

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**Program Specific Objectives (PSOs)**

S.No.	PSO Name	Program Specific Objective Statements
1	PSO - 1	To acquire knowledge in the area of Electrical and Electronics Engineering from Electrical Circuits, Electrical Machines, Control Systems, Measurements, Power Electronics to solve simple and advance problems arise in the same field.
2	PSO - 2	Apply appropriate techniques and modern tools in Electrical Engineering to be expert in multi-disciplinary areas and to engage in lifelong learning.
3	PSO - 3	Inculcate social environmental and sensibilities, ethical values and effective communication to provide engineering solutions as a services.



**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES, Telangana, India**

Mahatmanagar, LMD Colony, Thimmapur, Panthul Kondapur, Telangana 505527, India

Long 79.157328°

Lat 18.366018°

14/5/2022 01:50 PM

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AAK*  
Principal

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)





**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**BLOOM'S TAXONOMY DIGITAL PLANNING VERBS**

Definitions	Bloom's Definition	Action Verbs
Remembering (L1)	Exhibit memory of previously learned material by recalling facts, terms, basic concepts, and answers.	Choose, Define, Find, How, Label, List, Match, Name, Omit, Recall, Relate, Select, Show, Spell, Tell, What, When, Where, Which, Who, Why
Understanding (L2)	Demonstrate understanding of facts and ideas by organizing, comparing, translating, interpreting, giving descriptions, and stating main ideas.	Classify, Compare, Contrast, Demonstrate, Explain, Extend, Illustrate, Infer, Interpret, Outline, Relate, Rephrase, Show, Summarize, Translate
Applying (L3)	Solve problems to new situations by applying acquired knowledge, facts, techniques and rules in a different way.	Apply, Build, Choose, Construct, Develop, Experiment, with, Identify, Interview, Make use of, Model, Organize, Plan, Select, Solve, Utilize
Analyzing (L4)	Examine and break information into parts by identifying motives or causes. Make inferences and find evidence to support generalizations.	Analyze, Assume, Categorize, Classify, Compare, Conclusion, Contrast, Discover, Dissect, Distinguish, Divide, Examine, Function, Inference, Inspect, List, Motive, Relationships, Simplify, Survey, Take part in, Test for, Thesis
Evaluating (L5)	Present and defend opinions by making judgments about information, validity of ideas, or quality of work based on a set of criteria.	Agree, Appraise, Assess, Award, Choose, Compare, Conclude, Criteria, Criticize, Decide, Deduct, Defend, Determine, Disprove, Estimate, Evaluate, Explain, Importance, Influence, Interpret, Judge, Justify, Mark, Measure, Opinion, Perceive, Prioritize, Prove, Rate, Recommend, Rule on, Select, Support, Value
Creating (L6)	Compile information together in a different way by combining elements in a new pattern or proposing alternative solutions.	Adapt, Build, Change, Choose, Combine, Compile, Compose, Construct, Create, Delete, Design, Develop, Discuss, Elaborate, Estimate, Formulate, Happen, Imagine, Improve, Invent, Make up, Maximize, Minimize, Modify, Original, Originate, Plan, Predict, Propose, Solution, Solve, Suppose, Test, Theory

GPS Map Camera



**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES, Telangana, India**  
 KARIMNAGAR, THIMMAPUR, KARIMNAGAR, T.S. - 505527

75/G, LMD Colony, Thimmapur, Panthul Kondapur, Telangana 505527, India

Long 79.157997°

Lat 18.367681°

14/5/2022 01:52 PM

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*Principal*  
**Principal**

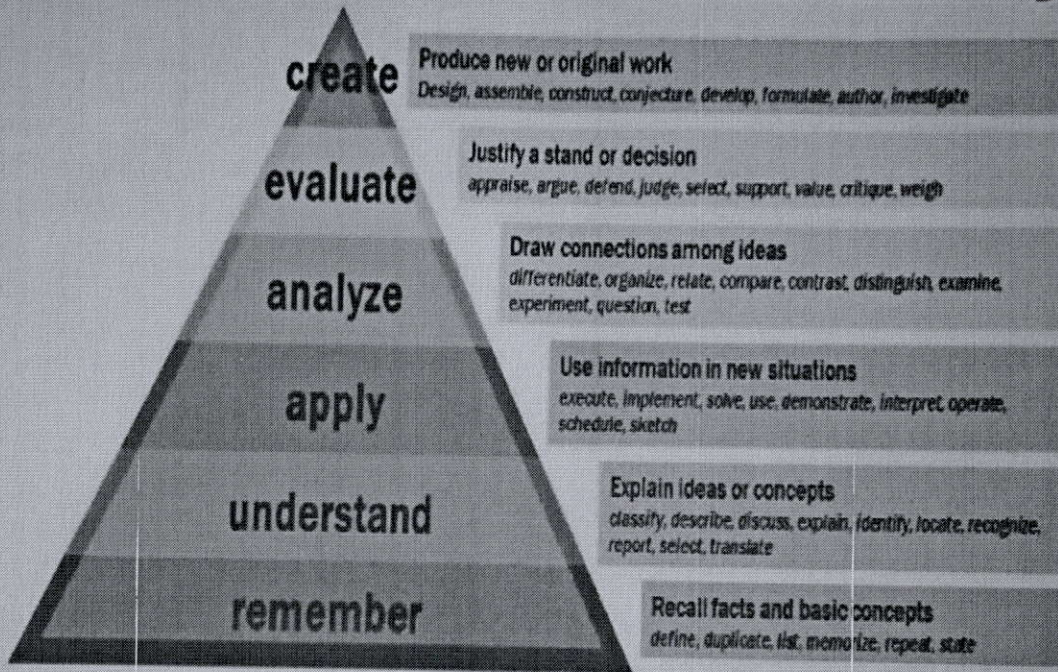
Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)





**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

**Bloom's Taxonomy**



**Bloom's taxonomy** is a set of three hierarchical models used for classification of educational learning objectives into levels of complexity and specificity. The three lists cover the learning objectives in cognitive, affective and psychomotor domains. The cognitive domain list has been the primary focus of most traditional education and is frequently used to structure curriculum learning objectives, assessments and activities.

The models were named after Benjamin Bloom, who chaired the committee of educators that devised the taxonomy. He also edited the first volume of the standard text, *Taxonomy of Educational Objectives: The Classification of Educational Goals*



**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES, Telangana, India**

0603 CVJ, LMD Dam Inspection Rd, LMD Colony, Thimmapur, Panthul Kondapur, Telangana 505527, India. KARIMNAGAR, T.S. - 505527

Long 79.154643°

Lat 18.371090°

14/5/2022 01:53 PM

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*Principal*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)



**SREE CHAITANYA**  
INSTITUTE OF TECHNOLOGICAL SCIENCES  
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**COURSE OUTCOME ATTAINMENT - INTERNAL ASSESSMENTS**

NAME OF THE FACULTY:		MRS.N.SUMA		ACADEMIC YEAR:		2022-23 R18		
BRANCH & SECTION:		ECE – A & B		EXAM:		MID - I		
COURSE :		155AV – DATACOMMUNICATIONS AND NETWORKS		YEAR / SEMESTER		III/I		
SI. NO.	ROLL NUMBER	QUESTION NO. Answer any two				OBJECTIVE	ASSIGNMENT	TOTAL
		1st	2nd	3rd	4th			
<b>MAXIMUM MARKS</b>		<b>5</b>	<b>5</b>	<b>5</b>	<b>5</b>	<b>10</b>	<b>5</b>	<b>25</b>
1	20TR1A0401	4	5			9	5	23
2	20TR1A0402	4	5			10	5	24
3	20TR1A0403	5			5	9	5	24
4	20TR1A0404	5	5			9	5	24
5	20TR1A0405	5	5			9	5	24
6	20TR1A0406	5			5	9	5	24
7	20TR1A0407				5	10	5	20
8	20TR1A0408	5	5			10	5	25
9	20TR1A0409	4	5			10	5	24
10	20TR1A0410	5	4			9	5	23
11	20TR1A0411	5				10	5	20
12	20TR1A0412	5	1			10	5	21
13	20TR1A0414	5	3			10	5	23
14	20TR1A0415						5	5
15	20TR1A0416		5		5	10	5	25
16	20TR1A0417		5			10	5	20
17	20TR1A0418		5			10	5	20
18	20TR1A0419	2	3			10	5	20
19	20TR1A0420		5		5	10	5	25
20	20TR1A0421	5			5	9	5	24
21	20TR1A0422	5				9	5	19
22	20TR1A0423	5				6	5	16
23	20TR1A0424	5			5	10	5	25
24	20TR1A0426	5			5	9	5	24
25	20TR1A0427	5				8	5	18
26	20TR1A0428	5				8	5	18
27	20TR1A0429	5			5	8	5	23
28	20TR1A0430				5	8	5	18

*AMC*  
Principal



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

29	20TR1A0431		5		4	9	5	23
30	20TR1A0432	5	5			8	5	23
31	20TR1A0434		5		5	8	5	23
32	20TR1A0435	5	5			8	5	23
33	20TR1A0436	3	4			9	5	21
34	20TR1A0437	4	5			10	5	24
35	20TR1A0438		5			8	5	18
36	20TR1A0439		5		5	8	5	23
37	20TR1A0440		5			8	5	18
38	20TR1A0441		5		5	8	5	23
39	20TR1A0442	5			5	8	5	23
40	20TR1A0443	5	4			8	5	22
41	20TR1A0444				5	8	5	18
42	20TR1A0445	5				10	5	20
43	20TR1A0446	5	5			9	5	24
44	20TR1A0447	5	3			8	5	21
45	20TR1A0448	5			5	8	5	23
46	20TR1A0449	5	3			8	5	21
47	20TR1A0450	5			5	10	5	25
48	20TR1A0451		5		5	8	5	23
49	20TR1A0452	5		5		8	5	23
50	20TR1A0453	4		5		8	5	22
51	20TR1A0454	5	5			8	5	23
52	20TR1A0455	5	5			9	5	24
53	20TR1A0457	5	4			9	5	23
54	20TR1A0458	5	5			10	5	25
55	20TR1A0459	3			4	7	5	19
56	20TR1A0460	4	5			9	5	23
57	20TR1A0461	5			5	10	5	25
58	20TR1A0462		5		5	9	5	24
59	20TR1A0463	2	5			9	5	21
60	20TR1A0465	5			4	9	5	23
61	20TR1A0466	5			5	9	5	24
62	20TR1A0467		5		5	10	5	25
63	20TR1A0468	5				10	5	20
64	20TR1A0469	5	3			9	5	22
65	20TR1A0470		5			9	5	19
66	20TR1A0471		5			9	5	19
67	20TR1A0473	5				8	5	18
68	20TR1A0474		5			7	5	17
69	20TR1A0475		5		4	7	5	21

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AKL*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi; Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

70	20TR1A0476		5		5	7	5	22
71	20TR1A0478	5			5	8	5	23
72	20TR1A0479	5			5	8	5	23
73	20TR1A0480	5			5	9	5	24
74	20TR1A0481	5			5	5	5	20
75	20TR1A0482	5				9	5	19
76	20TR1A0483	5			5	8	5	23
77	20TR1A0484	5	5			8	5	23
78	20TR1A0485				5	9	5	19
79	20TR1A0486	2	3			9	5	19
80	20TR1A0487	2	3			9	5	19
81	20TR1A0488	3	3			8	5	19
82	20TR1A0489	2.5	2.5			9	5	20
83	20TR1A0490		4		5	8	5	22
84	20TR1A0491		3	3		9	5	20
85	20TR1A0492	5	4			9	5	23
86	20TR1A0493		5		5	9	5	24
87	20TR1A0494	5				9	5	19
88	20TR1A0495	5	5			9	5	24
89	20TR1A0496	2	3			9	5	19
90	20TR1A0497		5			9	5	19
91	20TR1A0498	2.5	2.5			9	5	19
92	20TR1A0499	5			5	9	5	24
93	20TR1A04A0		5		5	10	5	25
94	20TR1A04A1	5	3			10	5	23
95	20TR1A04A2	2	3			8	5	18
96	20TR1A04A3		5			9	5	19
97	20TR1A04A4	5				9	5	19
98	20TR1A04A5	5			5	10	5	25
99	20TR1A04A6	5				9	5	19
100	20TR1A04A8	5	4			9	5	23
101	20TR1A04A9	5	5			9	5	24
102	20TR1A04B0	5	5			7	5	22
103	20TR1A04B2	5			5	8	5	23
104	20TR1A04B3				5	9	5	19
105	20TR1A04B5	5			5	8	5	23
106	20TR1A04B6	5	4			8	5	22
107	20TR1A04B8		5		5	9	5	21
108	20TR1A04B9		5		4	9	5	23
109	20TR1A04C0	5			5	10	5	25
110	21TR5A0401		5		5	9	5	24

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

111	21TR5A0402	5			5	9	5	24
112	21TR5A0403	5	5			9	5	24
113	21TR5A0405	5	5			10	5	25
114	21TR5A0406		5		5	10	5	25
115	21TR5A0407	5			5	9	5	24
116	21TR5A0408	5				9	5	19
117	21TR5A0409	4			3	8	5	20
118	21TR5A0410	5			3	8	5	21
119	21TR5A0411		5		5	9	5	24
120	21TR5A0412	4			5	8	5	22
<b>NO. OF STUDENTS ATTEMPTED</b>		<b>86</b>	<b>71</b>	<b>3</b>	<b>52</b>	<b>120</b>	<b>120</b>	<b>120</b>
<b>MARKS WISE QUESTION WISE</b>		<b>5</b>	<b>5</b>	<b>5</b>	<b>5</b>	<b>10</b>	<b>5</b>	<b>25</b>
<b>THRESHOLD 50%</b>		<b>2.5</b>	<b>2.5</b>	<b>2.5</b>	<b>2.5</b>	<b>5</b>	<b>2.5</b>	<b>14</b>
<b>NO. OF STUDENTS ABOVE THRESHOLD</b>		<b>79</b>	<b>68</b>	<b>3</b>	<b>52</b>	<b>120</b>	<b>120</b>	<b>120</b>
<b>% OF STUDENTS &gt; TARGET SCORE</b>		<b>91.86</b>	<b>95.78</b>	<b>100</b>	<b>100</b>	<b>100</b>	<b>100</b>	<b>100</b>
<b>ATTAINMENT LEVEL</b>		<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>

- ATTAINMENT LEVEL 1: 40% STUDENTS SCORE MORE THAN THRESHOLD**
- ATTAINMENT LEVEL 2: 50% STUDENTS SCORE MORE THAN THRESHOLD**
- ATTAINMENT LEVEL 3: 60% STUDENTS SCORE MORE THAN THRESHOLD**

COURSE OUTCOME MAPPING WITH EACH QUESTION						
CO'S	QUESTION NO.				OBJECTIVE	ASSIGNMENT
	1	2	3	4		
COURSE OUTCOME - 1	y				y	y
COURSE OUTCOME - 2		y		y	y	y
COURSE OUTCOME - 3			y		y	y
COURSE OUTCOME - 4						
COURSE OUTCOME - 5						

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)





COURSE OUTCOME ATTAINMENT BASED ON EXAM QUESTIONS IN TERMS OF PERCENTAGE OF TOTAL STUDENTS WHEN MAPPED ON EACH QUESTION						
CO'S	QUESTION NO.				OBJECTIVE	ASSIGNMENT
	1	2	3	4		
COURSE OUTCOME - 1	3				3	3
COURSE OUTCOME - 2		3		3	3	3
COURSE OUTCOME - 3			3		3	3
COURSE OUTCOME - 4						
COURSE OUTCOME - 5						

COURSE OUTCOME ATTAINMENT BASED ON EXAM QUESTIONS IN TERMS OF PERCENTAGE OF TOTAL STUDENTS WHEN MAPPED ON EACH QUESTION				
CO'S	SUBJECTIVE	OBJECTIVE	ASSIGNMENT	ATTAINMENT LEVEL
COURSE OUTCOME - 1	3	3	3	3
COURSE OUTCOME - 2	3	3	3	3
COURSE OUTCOME - 3	3	3	3	3
COURSE OUTCOME - 4				
COURSE OUTCOME - 5				

### MID-I QUESTION PAPER

**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES (TR)**  
III B.Tech I SEM I MID EXAMINATIONS-NOVEMBER-2022  
**DATA COMMUNICATIONS and NETWORKS**

Date: **11-11-2022 AN**  
 Branch: **ECE**

Time : **60min**  
 Max. Marks: **10**

Answer any ***TWO*** of the following:

1. State and explain basic network topologies and write advantages of each type.
2. Explain the ISO-OSI reference model.
3. Explain the CSMA/CD protocol.
4. Explain CRC method with example.

  
**Principal**

Sree Chaitanya Institute of Technological Science:  
 L.M.D. Colony, KARIMNAGAR (T.S)





**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES (TR)**  
III B.Tech. I Sem., I Mid Term Examinations, NOVEMBER – 2022  
**DATA COMMUNICATIONS and NETWORKS**  
**Objective Exam (ECE)**

Name: \_\_\_\_\_ Hall Ticket  
No.

--	--	--	--	--	--	--	--	--	--

Answer All Questions. All Questions Carry Equal Marks.

Time: 20 Min.  
Marks: 10.

**I. Choose the correct alternative:**

1. Communication between a computer and keyboard involves transmission. ( )  
a) Automatic                      b) Half-duplex                      c) Full-duplex                      d) Simplex
2. A set of rules that governs data communication \_\_\_\_ ( )  
a) Protocols                      b) Standards                      c) Agencies                      d) Forums
3. Which Topology requires a multipoint connection. ( )  
a) Bus                      b) Star                      c) Mesh                      d) Ring
4. Physical or logical arrangement of network is called ( )  
a) Routing                      b) Topology                      c) Networking                      d) None
5. In this topology there is a central controller ( )  
a) Mesh                      b) Ring                      c) Bus                      d) Star
6. A Local Area Network (LAN) is defined by ( )  
a) The geometric distance of the network                      b) The maximum number of hosts in the network  
c) The Topology of the network                      d) Standard of the network
7. The \_\_\_\_ layer is an end-to-end layer from source to destination ( )  
a) Network layer                      b) Data link layer                      c) Presentation layer                      d) Transport layer
8. Which transmission media has the highest transmission speed in a network ( )

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)





- a) Coaxial cable      b) Twisted pair cable      c) Optical fiber      d) None  
9. FCS standard in HDLC frame      (      )  
a) standard      b) frame      c) frame check sequence      d) None  
10. Noise less channel protocol is      (      )  
a) layered      b) simplest      c) Network layer      d) Transport layer

**II. Fill in the blanks:**

1. ISO stands for \_\_\_\_\_.
2. OSI stands for \_\_\_\_\_.
3. TCP/IP stands for \_\_\_\_\_.
4. The OSI model has \_\_\_\_\_ layers.
5. The TCP/IP Model has \_\_\_\_\_ layers.
6. ASCII code is the example of \_\_\_\_\_ standard.
7. In \_\_\_\_\_ transmission mode devices can transmit and receive simultaneously.
8. Throughput of slotted ALOHA \_\_\_\_\_.
9. Throughput of pure ALOHA \_\_\_\_\_.
10. I frames are designed for \_\_\_\_\_.

--@#O#@--



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES								
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING								
COURSE OUTCOME ATTAINMENT - INTERNAL ASSESSMENTS								
NAME OF THE FACULTY:		MRS.N.SUMA		ACADEMIC YEAR:		2022-23 R18		
BRANCH & SECTION:		ECE – A & B		EXAM:		MID - II		
COURSE :		155AV – DATA COMMUNICATIONS AND NETWORKS		YEAR / SEMESTER:		III/I		
SI. NO.	ROLL NUMBER	QUESTION NO. Answer any two				OBJECTIVE	ASSIGNMENT	TOTAL
		1st	2nd	3rd	4th			
<b>MAXIMUM MARKS</b>		5	5	5	5	10	5	25
1	20TR1A0401	5		5		8	5	23
2	20TR1A0402	5			5	8	5	24
3	20TR1A0403	5			5	8	5	24
4	20TR1A0404	5			5	8	5	24
5	20TR1A0405	4			5	9	5	24
6	20TR1A0406	5			5	9	5	24
7	20TR1A0407	3			5	9	5	21
8	20TR1A0408	5		5		8	4	24
9	20TR1A0409	5			5	7	3	23
10	20TR1A0410	2			5	7	3	21
11	20TR1A0411			5		8	4	19
12	20TR1A0412				5	8	4	20
13	20TR1A0414	5			5	8	5	23
14	20TR1A0415		3	5		9	5	22
15	20TR1A0416			3	5	7	4	23
16	20TR1A0417	5				8	5	19
17	20TR1A0418	5			5	8	5	22
18	20TR1A0419				4	5	5	14
19	20TR1A0420	5			5	9	3	25
20	20TR1A0421	5			5	7	4	23
21	20TR1A0422				5	7	2	18
22	20TR1A0423				5	6	5	16
23	20TR1A0424	5			5	7	5	24
24	20TR1A0426	5			5	9	4	24
25	20TR1A0427			5		7	5	18
26	20TR1A0428				5	7	5	18
27	20TR1A0429	5			5	/	5	23
28	20TR1A0430				5	7	5	18
29	20TR1A0431			5	5	7	5	23

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AK*

**Principal**  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

30	20TR1A0432	5			5	7	5	23
31	20TR1A0434	5			5	8	5	23
32	20TR1A0435	5			5	8	5	23
33	20TR1A0436	5			5	7	5	22
34	20TR1A0437	5			5	8	5	24
35	20TR1A0438	3			4	8	5	19
36	20TR1A0439	5			5	7	5	23
37	20TR1A0440	3			4	8	5	19
38	20TR1A0441	5			5	7	5	23
39	20TR1A0442			5	5	8	5	23
40	20TR1A0443	5			5	8	5	23
41	20TR1A0444	3			4	8	5	19
42	20TR1A0445	2			3	8	4	19
43	20TR1A0446	2.5			5	9	4	23
44	20TR1A0447	4			5	8	5	22
45	20TR1A0448	5		5		8	5	23
46	20TR1A0449	4			5	8	5	22
47	20TR1A0450	5			5	9	5	25
48	20TR1A0451	5			5	8	5	23
49	20TR1A0452	5			5	7	5	23
50	20TR1A0453	4			5	7	5	22
51	20TR1A0454			4	4	7	4	22
52	20TR1A0455			5	3	7	3	22
53	20TR1A0457	5			5	6	5	22
54	20TR1A0458	5			4	7	5	23
55	20TR1A0459			4	2	7	5	19
56	20TR1A0460			3	5	7	5	22
57	20TR1A0461	3			5	8	5	23
58	20TR1A0462			4	5	7	5	23
59	20TR1A0463	5			2	8	5	21
60	20TR1A0465	5			5	8	5	23
61	20TR1A0466		4		4	7	5	22
62	20TR1A0467		4	4		8	5	23
63	20TR1A0468	5	4			7	5	14
64	20TR1A0469			4	4	7	5	21
65	20TR1A0470			4	5	8	5	21
66	20TR1A0471			4	5	8	5	21
67	20TR1A0473	5			3	7	5	19
68	20TR1A0474		3	3		7	5	18
69	20TR1A0475		4		5	5	5	20
70	20TR1A0476		4		5	5	5	21

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*Principal*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

71	20TR1A0478	5			5	6	5	22
72	20TR1A0479			3	2	6	5	20
73	20TR1A0480	5			5	6	5	23
74	20TR1A0481		5		5	6	5	21
75	20TR1A0482				5	5	5	17
76	20TR1A0483	4			5	5	3	21
77	20TR1A0484		3		5	5	5	21
78	20TR1A0485	4			5	5	5	19
79	20TR1A0486			3	4	6	5	19
80	20TR1A0487	5				6	5	15
81	20TR1A0488			5	5	7	5	21
82	20TR1A0489			1	5	6	5	19
83	20TR1A0490		4	4		7	5	21
84	20TR1A0491			5	5	6	5	21
85	20TR1A0492	4		3		7	5	21
86	20TR1A0493		4		4	5	2	21
87	20TR1A0494		3		4	8	5	20
88	20TR1A0495	5			5	8	5	24
89	20TR1A0496	2			5	8	5	20
90	20TR1A0497			2	4	8	5	19
91	20TR1A0498				5	7	5	18
92	20TR1A0499	5			5	8	5	24
93	20TR1A04A0		5		5	6	5	23
94	20TR1A04A1	5			4	6	5	22
95	20TR1A04A2	1			5	7	5	18
96	20TR1A04A3			4	5	8	5	21
97	20TR1A04A4	3		3		7	5	19
98	20TR1A04A5			5	5	9	5	25
99	20TR1A04A6		3	4		6	5	19
100	20TR1A04A8		4		5	8	5	23
101	20TR1A04A9			4	5	8	5	23
102	20TR1A04B0	3		5		5	5	20
103	20TR1A04B2			5	5	8	5	23
104	20TR1A04B3	4			2	8	5	19
105	20TR1A04B5	5			5	7	3	23
106	20TR1A04B6			5	2	6	5	20
107	20TR1A04B8		5		5	6	5	23
108	20TR1A04B9		5		5	6	5	22
109	20TR1A04C0			5	5	8	5	24
110	21TR5A0401	5			5	8	5	23
111	21TR5A0402	5			5	8	5	23

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

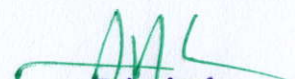
112	21TR5A0403	5			5	7	5	23
113	21TR5A0405	4			5	7	5	23
114	21TR5A0406	5			5	8	5	24
115	21TR5A0407			3	5	7	5	22
116	21TR5A0408	3	4			6	5	19
117	21TR5A0409		4	2		7	5	19
118	21TR5A0410	4			5	7	5	21
119	21TR5A0411		5	4		8	5	22
120	21TR5A0412	5			4	5	5	21
<b>NO. OF STUDENTS ATTEMPTED</b>		<b>68</b>	<b>19</b>	<b>39</b>	<b>100</b>	<b>120</b>	<b>120</b>	<b>120</b>
<b>MARKS WISE QUESTION WISE</b>		<b>5</b>	<b>5</b>	<b>5</b>	<b>5</b>	<b>10</b>	<b>5</b>	<b>25</b>
<b>THRESHOLD 50%</b>		<b>2.5</b>	<b>2.5</b>	<b>2.5</b>	<b>2.5</b>	<b>5</b>	<b>2.5</b>	<b>14</b>
<b>NO. OF STUDENTS ABOVE THRESHOLD</b>		<b>64</b>	<b>19</b>	<b>37</b>	<b>97</b>	<b>120</b>	<b>120</b>	<b>120</b>
<b>% OF STUDENTS&gt;TARGET SCORE</b>		<b>94.12</b>	<b>100</b>	<b>94.87</b>	<b>97</b>	<b>100</b>	<b>100</b>	<b>120</b>
<b>ATTAINMENT LEVEL</b>		<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>	<b>3</b>

ATTAINMENT LEVEL 1: 40% STUDENTS SCORE MORE THAN THRESHOLD

ATTAINMENT LEVEL 2: 50% STUDENTS SCORE MORE THAN THRESHOLD

ATTAINMENT LEVEL 3: 60% STUDENTS SCORE MORE THAN THRESHOLD

COURSE OUTCOME MAPPING WITH EACH QUESTION						
CO'S	QUESTION NO.				OBJECTIVE	ASSIGNMENT
	1	2	3	4		
COURSE OUTCOME - 1						
COURSE OUTCOME - 2						
COURSE OUTCOME - 3	3				3	3
COURSE OUTCOME - 4		3	3		3	3
COURSE OUTCOME - 5				3	3	3

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527





**COURSE OUTCOME ATTAINMENT BASED ON EXAM QUESTIONS IN TERMS OF PERCENTAGE OF TOTAL STUDENTS  
 WHEN MAPPED ON EACH QUESTION**

CO'S	QUESTION NO.				OBJECTIVE	ASSIGNMENT
	1	2	3	4		
COURSE OUTCOME - 1						
COURSE OUTCOME - 2						
COURSE OUTCOME - 3	y				y	y
COURSE OUTCOME - 4		y	y		y	y
COURSE OUTCOME - 5				y	y	y

**COURSE OUTCOME ATTAINMENT BASED ON EXAM QUESTIONS IN TERMS OF PERCENTAGE OF TOTAL STUDENTS  
 WHEN MAPPED ON EACH QUESTION**

CO'S	SUBJECTIVE	OBJECTIVE	ASSIGNMENT	ATTAINMENT LEVEL
COURSE OUTCOME - 1				
COURSE OUTCOME - 2				
COURSE OUTCOME - 3	3	3	3	3
COURSE OUTCOME - 4	3	3	3	3
COURSE OUTCOME - 5	3	3	3	3

**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES (TR)**  
III B.Tech I SEM II MID EXAMINATIONS-JANUARY-2023  
**DATA COMMUNICATIONS AND NETWORKS**

Date: **19-01-2023 AN**  
 Branch: **ECE**

Time: **60min**  
 Max.Marks: **10**

Answer any **TWO** of the following:

1. Explain Services provided by the Transport layer?
2. Explain TCP Header Format
3. What is Logical Addressing? Give The Services And Header Format Of IPV4.
4. Explain the Concept Of Email, Its Architecture And Services.

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES (TR)**  
B.Tech. III Year I Sem., II Mid Term Examinations, JANUARY – 2023  
**DATA COMMUNICATIONS AND NETWORKS**  
**Objective Exam (ECE)**

Name: \_\_\_\_\_ Hall Ticket No. \_\_\_\_\_

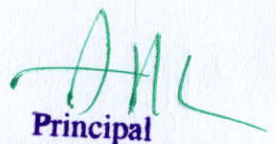
--	--	--	--	--	--	--	--	--	--

Answer All Questions. All Questions Carry Equal Marks.

Time: 20Min  
Marks: 10

**II. Choose the correct alternative:**

- The network layer concerns with \_\_\_\_\_ ( )  
a) Bits            b) frames            c) packets            d) bytes
- The 4 byte IP address consists of \_\_\_\_\_ ( )  
a) Network address            b) host address            c) both a & b            d) none
- In \_\_\_\_\_ protocol the station transmits with a probability of 1 whenever it finds the channel idles. ( )  
a) 1- persistent CSMA            b) p- persistent CSMA            c) non- persistent CSMA  
d) none
- Which protocol version uses Hexadecimal address format \_\_\_\_\_ ( )  
a) IPV4            b) IPV6            c) both            d) none
- Protocols in which stations listen for a carrier and act accordingly are \_\_\_\_\_ ( )  
a) ALOHA            b) multiple access            c) station model            d) CSMA
- Which one of the following task is not done by data link layer \_\_\_\_\_ ( )  
a) Framing            b) error control            c) flow control            d) routing
- FTP uses control connection of TCP Port \_\_\_\_\_ ( )  
a) 20            b)21            c)22            d)23
- SMTP uses TCP port number \_\_\_\_\_ ( )  
a) 25 b)26 c)27 d)28
- IPV4 protocol has the address length of \_\_\_\_\_ ( )  
a) 32 bits            b)64 bits            c)128 bits            d)256 bits
- 10 large organizations with large number of hosts are design with IP addresses of \_\_\_\_\_ ( )  
a) Class - A            b)class - B            c)class-C            d) class-D



Principal

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)





**III. Fill in the blanks:**

11. Network \_\_\_\_\_ occur in case of traffic overloading
12. Routing is a function of a \_\_\_\_\_ layer
13. \_\_\_\_\_ is the main protocol used in electronic mail(e-mail) service
14. HTTP stands for \_\_\_\_\_
15. World wide web uses \_\_\_\_\_ interaction
16. The DNS name space is \_\_\_\_\_ and it is similar to the UNIX file system
17. STTP stands for \_\_\_\_\_
18. FTP stands for \_\_\_\_\_
19. SMTP stands for \_\_\_\_\_
20. SNMP stands for \_\_\_\_\_

---#o@O@o#---

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

<b>SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES</b>			
<b>DEPARTMENT</b>		<b>ELECTRONICS AND COMMUNICATION ENGINEERING</b>	
<b>COURSE OUTCOME ATTAINMENT EXTERNAL EXAMINATION</b>			
<b>NAME OF THE FACULTY:</b>	<b>MRS.N.SUMA</b>	<b>ACADEMIC YEAR :</b>	<b>2022-23</b>
<b>BRANCH &amp; SECTION:</b>	<b>ECE – A &amp; B</b>	<b>EXAM:</b>	<b>EXTERNAL</b>
<b>COURSE:</b>	<b>155AV – DATACOMMUNICATIONS AND NETWORKS</b>	<b>YEAR/SEMESTER:</b>	<b>III/I</b>

S.NO.	HALL TICKET NO.	EXTERNAL MARKS	TOTAL(MAX. POINTS:10)
1.	20TR1A0401	26	5
2.	20TR1A0402	32	6
3.	20TR1A0403	26	6
4.	20TR1A0404	46	8
5.	20TR1A0405	28	6
6.	20TR1A0406	26	6
7.	20TR1A0407	35	6
8.	20TR1A0408	36	7
9.	20TR1A0409	30	6
10.	20TR1A0410	31	6
11.	20TR1A0411	26	5
12.	20TR1A0412	43	7
13.	20TR1A0414	43	7
14.	20TR1A0415	38	6
15.	20TR1A0416	26	6
16.	20TR1A0417	13	0
17.	20TR1A0418	28	5
18.	20TR1A0419	26	5
19.	20TR1A0420	26	6
20.	20TR1A0421	31	6
21.	20TR1A0422	26	5
22.	20TR1A0423	41	6
23.	20TR1A0424	37	7
24.	20TR1A0426	37	7
25.	20TR1A0427	41	6
26.	20TR1A0428	27	5
27.	20TR1A0429	30	6
28.	20TR1A0430	26	5
29.	20TR1A0431	26	5
30.	20TR1A0432	32	6
31.	20TR1A0434	42	7
32.	20TR1A0435	33	6
33.	20TR1A0436	38	7

**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



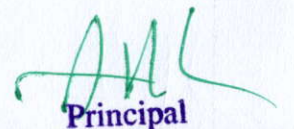
# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

34.	20TR1A0437	42	7
35.	20TR1A0438	26	5
36.	20TR1A0439	34	6
37.	20TR1A0440	27	5
38.	20TR1A0441	27	6
39.	20TR1A0442	36	6
40.	20TR1A0443	28	6
41.	20TR1A0444	30	5
42.	20TR1A0445	31	6
43.	20TR1A0446	37	7
44.	20TR1A0447	32	6
45.	20TR1A0448	37	7
46.	20TR1A0449	38	7
47.	20TR1A0450	32	6
48.	20TR1A0451	35	6
49.	20TR1A0452	31	6
50.	20TR1A0453	32	6
51.	20TR1A0454	40	7
52.	20TR1A0455	41	7
53.	20TR1A0457	44	7
54.	20TR1A0458	29	6
55.	20TR1A0459	36	6
56.	20TR1A0460	39	7
57.	20TR1A0461	50	8
58.	20TR1A0462	39	7
59.	20TR1A0463	30	6
60.	20TR1A0465	46	7
61.	20TR1A0466	29	6
62.	20TR1A0467	42	7
63.	20TR1A0468	33	6
64.	20TR1A0469	27	5
65.	20TR1A0470	39	6
66.	20TR1A0471	26	5
67.	20TR1A0473	33	6
68.	20TR1A0474	33	6
69.	20TR1A0475	35	6
70.	20TR1A0476	38	7
71.	20TR1A0478	36	6
72.	20TR1A0479	26	5
73.	20TR1A0480	38	7
74.	20TR1A0481	28	5
75.	20TR1A0482	33	6
76.	20TR1A0483	46	7
77.	20TR1A0484	33	6

  
Principal

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

78.	20TR1A0485	26	5
79.	20TR1A0486	44	7
80.	20TR1A0487	27	5
81.	20TR1A0488	28	5
82.	20TR1A0489	39	6
83.	20TR1A0490	34	6
84.	20TR1A0491	30	6
85.	20TR1A0492	36	6
86.	20TR1A0493	33	6
87.	20TR1A0494	27	5
88.	20TR1A0495	37	7
89.	20TR1A0496	37	6
90.	20TR1A0497	26	5
91.	20TR1A0498	27	5
92.	20TR1A0499	30	6
93.	20TR1A04A0	48	8
94.	20TR1A04A1	27	6
95.	20TR1A04A2	26	5
96.	20TR1A04A3	26	5
97.	20TR1A04A4	35	6
98.	20TR1A04A5	43	7
99.	20TR1A04A6	27	5
100.	20TR1A04A8	41	7
101.	20TR1A04A9	29	6
102.	20TR1A04B0	36	6
103.	20TR1A04B2	37	7
104.	20TR1A04B3	33	6
105.	20TR1A04B5	38	7
106.	20TR1A04B6	35	6
107.	20TR1A04B8	30	6
108.	20TR1A04B9	42	7
109.	20TR1A04C0	51	8
110.	21TR5A0401	33	6
111.	21TR5A0402	42	7
112.	21TR5A0403	45	7
113.	21TR5A0405	40	7
114.	21TR5A0406	43	7
115.	21TR5A0407	47	8
116.	21TR5A0408	33	6
117.	21TR5A0409	30	6
118.	21TR5A0410	35	6
119.	21TR5A0411	48	8
120.	21TR5A0412	36	6

*LMD*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)

**LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527**



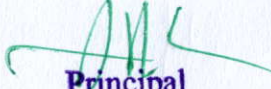
**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

<b>NO. OF STUDENTS WHO ATTEMPTED THE SUBJECT</b>	<b>120</b>
<b>MAX. MARKS</b>	<b>75</b>
<b>THRESHOLD 40%</b>	<b>30</b>
<b>NO. OF STUDENTS WHO SCORED MORE THAN TARGET SCORE</b>	<b>85</b>
<b>PERCENTAGE OF STUDENTS WHO SCORED MORE THAN TARGET SCORE</b>	<b>70.83</b>
<b>OVERALL EXTERNAL ATTAINMENT LEVEL</b>	<b>3</b>

- ATTAINMENT LEVEL 1: 40% STUDENTS SCORE MORE THAN THRESHOLD**
- ATTAINMENT LEVEL 2: 50% STUDENTS SCORE MORE THAN THRESHOLD**
- ATTAINMENT LEVEL 3: 60% STUDENTS SCORE MORE THAN THRESHOLD**

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



<b>END OF COURSE EVALUATION FORM</b>					
<b>SUBJECT:</b>			<b>DATA COMMUNICATIONS AND NETWORKS</b>		
<b>DEPARTMENT &amp; SECTION:</b>			<b>ECE- A &amp; B</b>		
<b>ACADEMIC YEAR:</b>			<b>2022-23</b>		
<b>FACULTY NAME:</b>			<b>MRS.N.SUMA</b>		
<b>DESIGNATION:</b>			<b>ASSISTANT PROFESSOR</b>		
<b>COURSE OUTCOMES : (AT THE END OF THE COURSE THE STUDENT IS ABLE TO)</b>		<b>TO A GREAT EXTENT (HIGH )</b>	<b>TO A MODERATE EXTENT(MEDIUM)</b>	<b>TO SOME EXTENT (LOW)</b>	<b>OUT OF 3</b>
		<b>(3)</b>	<b>(2)</b>	<b>(1)</b>	
<b>1</b>	Know the Categories and functions of various Data communication Networks	98	22	0	2.87
<b>2</b>	Design and analyze various error detection techniques	84	36	0	2.79
<b>3</b>	Demonstrate the mechanism of routing the data in network layer	75	45	0	2.75
<b>4</b>	Know the significance of various Flow control and Congestion control Mechanisms	87	30	3	2.66
<b>5</b>	Know the Functioning of various Application layer Protocols.	91	27	2	2.85
<b>OVERALL RATING</b>		<b>OUT OF 3</b>			<b>2.79</b>

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES			
DEPARTMENT:		ELECTRICAL AND ELECTRONICS ENGINEERING	
OVERALL COURSE OUTCOME ATTAINMENT			
NAME OF THE FACULTY:	MRS.N.SUMA	ACADEMIC YEAR:	2022-23
BRANCH & SECTION:	ECE A & B	EXAM:	INTERNAL & EXTERNAL
COURSE:	DATA COMMUNICATIONS AND NETWORKS	SEMESTER:	III/I

COURSE OUTCOMES	1 <sup>ST</sup> INTERNAL EXAM	2 <sup>ND</sup> INTERNAL EXAM	INTERNAL EXAM (AVG.)	UNIVERSITY EXAM	OVERALL ATTAINMENT
COURSE OUTCOME - 1	3		3	3	3
COURSE OUTCOME - 2	3		3	3	3
COURSE OUTCOME - 3	3	3	3	3	3
COURSE OUTCOME - 4		3	3	3	3
COURSE OUTCOME - 5		3	3	3	3
<b>AVERAGE</b>				<b>3</b>	<b>3</b>

FINAL CO DIRECT ATTAINMENT FOR THE SUBJECT	3
FINAL CO INDIRECT ATTAINMENT FOR THE SUBJECT	2.79
FINAL CO ATTAINMENT FOR THE SUBJECT	2.86

*AAL*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)



**Attainment Levels In Case Of Grade System**

Internal Assessment	External Assessment
Attainment Level 1: 50% Students $\geq 14$	Attainment Level 1: 50% Students $\geq$ Pass
Attainment Level 2: 60% Students $\geq 14$	Attainment Level 2: 60% Students $\geq$ Pass
Attainment Level 3: 70% Students $\geq 14$	Attainment Level 3: 70% Students $\geq$ Pass

**Direct Attainment Calculation**

Academic Year	Subject Code-Subject Name	Program-Year-Sem-Branch	Faculty Name
2022-23	181AB - BASIC ELECTRICAL ENGINEERING	B.Tech-I-I-CSE	M.SRIKANTH

S.No.	Hall Ticket No.	INTERNAL Average of Mids (40Marks)	EXTERNAL (Grade Points)
1	22TR1A0501	37	7
2	22TR1A0502	37	7
3	22TR1A0503	33	0
4	22TR1A0504	37	7
5	22TR1A0505	40	7
6	22TR1A0506	40	7
7	22TR1A0507	31	6
8	22TR1A0508	33	6
9	22TR1A0509	35	6
10	22TR1A0510	35	0
11	22TR1A0511	33	0
12	22TR1A0512	39	7
13	22TR1A0513	36	7
14	22TR1A0514	32	6
15	22TR1A0515	32	6
16	22TR1A0516	39	7
17	22TR1A0517	39	8
18	22TR1A0518	38	0
19	22TR1A0519	33	6
20	22TR1A0520	36	6
21	22TR1A0521	36	7
22	22TR1A0522	30	0
23	22TR1A0523	40	8



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

24	22TR1A0524	38	8
25	22TR1A0525	35	7
26	22TR1A0526	38	7
27	22TR1A0527	39	7
28	22TR1A0528	37	7
29	22TR1A0529	38	7
30	22TR1A0530	35	7
31	22TR1A0531	31	6
32	22TR1A0533	40	8
33	22TR1A0534	19	5
34	22TR1A0535	19	5
35	22TR1A0536	33	7
36	22TR1A0537	34	6
37	22TR1A0538	33	6
38	22TR1A0539	30	0
39	22TR1A0540	36	6
40	22TR1A0541	33	6
41	22TR1A0542	32	0
42	22TR1A0543	35	0
43	22TR1A0544	33	6
44	22TR1A0545	19	0
45	22TR1A0546	10	0
46	22TR1A0547	31	0
47	22TR1A0548	33	0
48	22TR1A0549	35	6
49	22TR1A0550	35	6
50	22TR1A0551	39	7
51	22TR1A0552	38	7
52	22TR1A0553	37	7
53	22TR1A0554	36	6
54	22TR1A0555	40	7
55	22TR1A0556	39	7
56	22TR1A0557	37	0
57	22TR1A0558	34	6
58	22TR1A0559	33	0
59	22TR1A0560	31	6
60	22TR1A0561	37	7
61	22TR1A0562	35	7

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
Principal

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

62	22TR1A0563	32	0
63	22TR1A0564	37	8
64	22TR1A0565	30	0
65	22TR1A0566	30	0
66	22TR1A0567	31	0
67	22TR1A0568	35	7
68	22TR1A0569	35	7
69	22TR1A0570	28	0
70	22TR1A0571	33	0
71	22TR1A0572	31	0
72	22TR1A0573	30	0
73	22TR1A0574	28	5
74	22TR1A0575	35	0
75	22TR1A0576	30	0
76	22TR1A0577	34	0
77	22TR1A0578	34	7
78	22TR1A0579	34	7
79	22TR1A0580	38	8
80	22TR1A0581	40	8
81	22TR1A0582	30	0
82	22TR1A0583	30	0
83	22TR1A0584	40	7
84	22TR1A0585	33	8
85	22TR1A0586	31	6
86	22TR1A0587	39	9
87	22TR1A0588	31	0
88	22TR1A0589	33	7
89	22TR1A0590	33	0
90	22TR1A0591	36	7
91	22TR1A0592	38	7
92	22TR1A0593	37	7
93	22TR1A0594	36	7
94	22TR1A0595	33	7
95	22TR1A0596	35	7
96	22TR1A0597	33	0
97	22TR1A0598	29	6
98	22TR1A0599	36	7
99	22TR1A05A0	27	0

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

100	22TR1A05A2	40	8
101	22TR1A05A3	34	7
102	22TR1A05A4	26	5
103	22TR1A05A5	28	0
104	22TR1A05A6	40	7
105	22TR1A05A7	32	6
106	22TR1A05A8	38	7
107	22TR1A05A9	29	0
108	22TR1A05B0	36	7
109	22TR1A05B1	40	9
110	22TR1A05B2	35	0
111	22TR1A05B3	36	9
112	22TR1A05B4	40	8
113	22TR1A05B5	31	6
114	22TR1A05B6	37	6
115	22TR1A05B7	35	8
116	22TR1A05B8	37	7
117	22TR1A05B9	36	6
118	22TR1A05C0	30	6
119	22TR1A05C1	37	7
120	22TR1A05C2	38	6
121	22TR1A05C3	39	7
122	22TR1A05C4	37	7
123	22TR1A05C5	32	6
124	22TR1A05C6	40	9
125	22TR1A05C7	36	8
126	22TR1A05C8	39	7

**No. of students attempted : 126**

**Course Attainment Calculation :**

Attainment	No. of students reached target	%Target Attainment	Attainment Level
Internal Attainment (h)	126	100	3
External Attainment (a)	92	73	3

**Course Attainment :  $(0.60*a+0.40*b) = 3$**

**COA = 3**

**LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527**

*Principal*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

	PO1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO 1	PSO2	PSO3
CO1	3	2	1								2	2	3	1	1
CO2	3	2	1						1		2	2	3	1	1
CO3	3	2	1			1			1		1	2	3	2	1
CO4	3	2	1			1			1		1	2	3	2	0
CO5	3	2	1			2			1		2	2	1	2	1
AVERAGE	3	2	1			1.33			1.33		1.6	2	2.6	1.6	1.33

PO Attainment level calculation =  $(1/3) * \text{Course Attainment level}$

PO/PSO Attainment=  $\text{COA} \times \text{M}/3$

EXAMPLE: FOR PO2 :  $3 \times 2/3 = 2$

PO1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO1	PSO2	PSO3
3	2	1			1.33			1.33		1.6	2	2.6	1.6	1.33

*AAC*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S)



**Attainment Levels In Case Of Grade System**

Internal Assessment	External Assessment
Attainment Level 1: 50% Students $\geq 14$	Attainment Level 1: 50% Students $\geq$ Pass
Attainment Level 2: 60% Students $\geq 14$	Attainment Level 2: 60% Students $\geq$ Pass
Attainment Level 3: 70% Students $\geq 14$	Attainment Level 3: 70% Students $\geq$ Pass

**Direct Attainment Calculation**

Academic Year	Subject Code-Subject Name	Program-Year-Sem-Branch	Faculty Name
2022-23	181AP - PROGRAMMING FOR PROBLEM SOLVING	B.Tech-I-I-AIML	N.MAHESH

S.No.	Hall Ticket No.	INTERNAL Average of Mids (40Marks)	EXTERNAL (Grade Points)
1	22TR1A7301	31	6
2	22TR1A7302	31	6
3	22TR1A7303	34	7
4	22TR1A7304	34	6
5	22TR1A7305	32	6
6	22TR1A7306	36	6
7	22TR1A7307	33	6
8	22TR1A7308	30	7
9	22TR1A7309	31	6
10	22TR1A7310	38	7
11	22TR1A7311	36	8
12	22TR1A7312	35	6
13	22TR1A7313	35	6
14	22TR1A7314	34	7
15	22TR1A7315	38	7
16	22TR1A7316	34	7
17	22TR1A7317	35	6
18	22TR1A7318	39	7
19	22TR1A7319	34	6
20	22TR1A7320	37	7
21	22TR1A7321	36	7
22	22TR1A7322	35	6
23	22TR1A7323	36	7
24	22TR1A7324	30	6



# SREE CHAITANYA

INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

25	22TR1A7325	32	6
26	22TR1A7326	38	7
27	22TR1A7327	36	6
28	22TR1A7328	36	7
29	22TR1A7329	26	5
30	22TR1A7330	30	6
31	22TR1A7331	37	7
32	22TR1A7332	32	6
33	22TR1A7333	37	7
34	22TR1A7334	31	6
35	22TR1A7335	33	6
36	22TR1A7336	34	6
37	22TR1A7337	31	6
38	22TR1A7338	35	6
39	22TR1A7339	32	7
40	22TR1A7340	24	6
41	22TR1A7341	34	7
42	22TR1A7342	34	6
43	22TR1A7343	32	7
44	22TR1A7344	30	6
45	22TR1A7345	33	6
46	22TR1A7346	35	7
47	22TR1A7347	27	5
48	22TR1A7348	36	6
49	22TR1A7349	34	6
50	22TR1A7350	30	6
51	22TR1A7351	28	6
52	22TR1A7352	31	6
53	22TR1A7353	34	6
54	22TR1A7354	26	5
55	22TR1A7355	28	5
56	22TR1A7356	33	7
57	22TR1A7357	30	6
58	22TR1A7358	36	8
59	22TR1A7359	32	6
60	22TR1A7360	38	7
61	22TR1A7361	36	7
62	22TR1A7362	32	6

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

Principal  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)





63	22TR1A7363	20	0
----	------------	----	---

**No. of students attempted : 63**

**Course Attainment Calculation :**

	No. of students reached target	%Target Attainment	Attainment Level
Internal Attainment (b)	63	100	3
External Attainment (a)	62	98.41	3

**Course Attainment :  $(0.60*a+0.40*b) = 3$**

**COA=3**

	PO1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO1	PSO2	PSO3
CO1	3	2	1	2	2				1	1	1	2		1	
CO2	3	3	2	2	2				1	2	1	2		1	
CO3	3	3	2	2	2				1	1	1	2		1	
CO4	3	3	3	2	3				1	1	1	2		1	
CO5	3	3	3	2	3				1	1	1	2		1	
AVERAGE (M)	3	2.8	2.2	2	2				1	1	1	2		1	

**PO Attainment level calculation =  $(1/3) * \text{Course Attainment level}$**

**PO/PSO Attainment=  $\text{COA} \times \text{M}/3$**

**EXAMPLE: FOR PO2 :  $3*2.8/3 = 2.8$**

PO1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO1	PSO2	PSO3
3	2.8	2.2	2	2				1	1	1	2		1	

Similarly CO Attainment is calculated for all theory courses for corresponding academic years respectively

**Co Attainment Level For Courses Other Than Theory :**

a) Laboratory:

Continuous Internal Evaluation:

The internal evaluation is based on session wise performance of experiment and viva voce, observation, record and internal examination. The final internal marks are considered for CIE and CO level for CIE attainment is decided upon the percentage of students who score more than 55% of the maximum internal marks, i.e., 13.75 out of 25 is used to decide the CO attainment level and is uniform for all CO's.

**LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527**

**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR





Semester End University Examination Evaluation (SEE): SEE Lab exam is evaluated for 75 marks. In SEE of the lab, % of students who score over 40% of the maximum marks, i.e., 30 out of 75 marks, is used to decide the CO attainment level and is uniform for all CO's.

**b) Seminar:**

For seminars, the assessment is based only on internal evaluation. The marks obtained in seminar is used to decide the % of students who scored more than 55 % of maximum marks, and this % is used for determining corresponding CO attainment level. This attainment level is construed as uniform for all Cos of the course.

**c) Industry Oriented Mini Project:**

For Industry Oriented Mini Project the assessment is based only on External evaluation. The marks obtained in Industry Oriented Mini Project is used to decide the % of students who secured more than 40 % of maximum marks, and this % is used for determining corresponding CO attainment level. This attainment level is construed as uniform for all Cos of the course.

**d) Project: Continuous Internal Evaluation:**

The internal marks for project (25) are the total of marks allotted in Project review, final presentation and by project guide. The final internal marks are considered for CIE and CO level for CIE attainment is decided upon the percentage of students who score more than 55% of the maximum internal marks, i.e., 13.75 out of 25 is used to decide the CO attainment level and is uniform for all Cos.

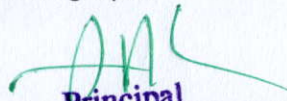
**Semester End University Examination Evaluation (SEE):**

The external evaluation is by award of Grade (Excellent/Very Good/Satisfactory/Poor). These grades are considered for SEE and CO level for SEE attainment is decided upon the percentage of students who got Excellent?/Very Good/Good to the number of students appeared is used to decide the CO attainment level and is uniform for all OCs. The average value of the CO levels for the course are then used for mapping the PO attainments, using the array of target PO values for the course.

**Attainment of Program Outcomes and Program Specific Outcomes:**

Direct assessment of Pos and PSOs for a course is obtained by mapping the respective CO value of Course Outcome attainment with the mapping of the target or expected PO's and PSO's for the particular course. The procedure adapted for calculating the attainment of Course Outcomes for a Theory course is described with an example for the course Python Programming the CO values arrived from CIE and SEE for the course Python Programming .

The PO/PSO's are attained indirectly by taking different surveys. These surveys are also having equal weightages.

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



**For overall PO/PSO attainment :**

1. Program Exit Survey
2. Alumni Survey
3. Employer Feedback Survey

**Frequency of Data Collection:**

**Table : Data Frequency**

S.NO.	Assessment Tool	Frequency of Data Collection
1.	Program Exit Survey	Once a Year
2.	Alumni Survey	Once a Year
3.	Employer Feedback Survey	Once a Year

**Indirect Assessment Tools:**

**Alumni Survey:** A feedback is collected on PO'S and PSO'S from Alumni students. It contributes towards the weightage of PO and PSO attainment. This survey is conducted by Alumni coordinator with the passed out students. Alumni coordinator collects both Alumni feedback forms filled by passed out student.

**Employer Feedback Survey:** A feedback is collected on Vision & Mission, PEO's, and PO's & PSO's. It is an indirect assessment tool which contributes towards the weightage of PO and PSO's. It is conducted after one year of service completed by the graduates from joining those respective organizations.

**Program Exit Survey :** The Program graduate exit survey is collected at the end of the program. The objective of the survey is to know the level of confidence of each PO's and PSO's that a graduate possess by the end of the program.



**Principal**  
Sree Chaitanya Institute of Technological Sci:  
L.M.D. Colony, KARIMNAGAR (T.:





**STUDENTS FEEDBACK ON FACULTY**

Year & Semester	II - I	Section	A
Academic Year	2018 - 19	Department	EEE

**1.4.1: FEEDBACK ANALYSIS OF STUDENT FEEDBACK ON FACULTY**

Dear Students, kindly rate the following points for all your faculties (Subject Wise), Apply GRADE Mark with 4-Excellent, 3-Good, 2-Satisfactory, 1-Poor

S.NO	Description	SUB-1	SUB-2	SUB-3	SUB-4	SUB-5	SUB-6
		ML	EMF	EM-I	NT	EE	
1	Knowledge of the Subject	4	3	4	3	4	
2	Preparedness of the Class	4	3	4	3	4	
3	Maintain class Discipline	3	4	4	3	4	
4	Time Sense	4	3	3	4	3	
5	Syllabus Coverage	3	4	3	4	3	
6	Clarifying student queries	3	4	3	4	3	
7	Communication Skills	4	3	4	4	4	
8	Understanding Level	4	3	4	4	4	
9	Voice Quality	4	3	4	4	4	

Dear Students, kindly rate the following points for all your faculties (Lab wise), Apply GRADE Mark with 4-Excellent, 3-Good, 2-Satisfactory, 1-Poor

Lab Courses : [Laboratory Name]	GRADE
10 EM-I Lab	
11 NT Lab	
12 EDC	
13	

Suggestions (If any) :

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*Principal*  
 Sree Chaitanya Institute of Technological Science  
 L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

## SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

STUDENTS FEEDBACK ON FACULTY							
Year & Semester		III - II	Section		A		
Academic Year		2018-19	Department		EEE		
1.4.1: FEEDBACK ANALYSIS OF STUDENT FEEDBACK ON FACULTY							
Dear Students, Kindly rate the following points for all your faculties (Subject Wise), Apply GRADE Mark with 4-Excellent, 3-Good, 2-Satisfactory, 1-Poor							
S.NO	Description	SUB-1	SUB-2	SUB-3	SUB-4	SUB-5	SUB-6
		PSA	PE	SEP	NCEC	LDICA	
1	Knowledge of the Subject	3	4	4	4	3	
2	Preparedness of the Class	4	3	3	4	4	
3	Maintain class Discipline	3	4	4	3	3	
4	Time Sense	4	3	3	4	4	
5	Syllabus Coverage	3	4	4	3	3	
6	Clarifying student queries	4	3	3	4	4	
7	Communication Skills	3	4	4	3	3	
8	Understanding Level	4	3	3	4	4	
9	Voice Quality	3	4	4	4	3	
Dear Students, Kindly rate the following points for all your faculties (Lab wise), Apply GRADE Mark with 4-Excellent, 3-Good, 2-Satisfactory, 1-Poor							
Lab Courses : (Laboratory Name)		GRADE					
10	PE	4					
11	PS	4					
12	AECS	3					
13							
Suggestions (If any) :							

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
Principal  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

## SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



SREE CHAITANYA  
EDUCATIONAL INSTITUTIONS

### FEEDBACK FROM TEACHING FACULTY

NAME OF THE FACULTY	V. Praveen Kumar	EMPLOYEE ID	10333
DESIGNATION	Assoe. Professor	GENDER	M
DEPARTMENT	ECE	DATE	13/4/2020
EMAIL ID	praveen.voladri@gmail.com	ACADEMIC YEAR	2019-2020

5 - EXCELLENT	4 - VERY GOOD	3 - GOOD	2 - AVERAGE	1 - POOR
---------------	---------------	----------	-------------	----------

S.NO.	STATEMENTS	EXCELLENT	VERY GOOD	GOOD	AVERAGE	POOR
1	How do you rate the Higher education facility available under Career Advancement Scheme (CAS)?	5				
2	Increment after achieving higher degree/certificate		4			
3	Award for best performance in academics?	5				
4	Research environment in Institute		4			
5	Satisfaction with regular appraisal system	5				
6	Rotation method to attend FDP/Workshops/Seminar	5				
7	How do you rate Management for providing financial benefits to FDP/Workshop/Conference/Paper presentation	5	4			
8	Teaching facilities in class room	5				
9	How do you rate the accidental Insurance Policy	5				
10	Library facility in the college	5				
11	How do you rate Management for providing Transportation facility?	5				
12	Working environment of department/Institute	5				
13	Satisfaction with the Management	5				
14	Canteen facility in the campus		4			

Overall Rating = (No of Points/70\*100) =  $\frac{66}{70} \times 100 = 94.27$

*V. Praveen Kumar*  
Signature of the Faculty

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AAC*  
Principal  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

## SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

### FEEDBACK FROM TEACHING FACULTY

NAME OF THE FACULTY	SPVENIKAT RAMKUMAR	EMPLOYEE ID	10321
DESIGNATION	ASSOC PROFESSOR	GENDER	MALE
DEPARTMENT	H&S	DATE	19/9/2022
EMAIL ID	spvramkumar@	ACADEMIC YEAR	2022-23

5 - EXCELLENT	4 - VERY GOOD	3 - GOOD	2 - AVERAGE	1 - POOR
---------------	---------------	----------	-------------	----------

S.NO	STATEMENTS	EXCELLENT	VERY GOOD	GOOD	AVERAGE	POOR
1	How do you rate the Higher education facility available under Career Advancement Scheme (CAS)?	5				
2	Increment after achieving higher degree/certificate	5				
3	Award for best performance in academics?	5				
4	Research environment in Institute		4			
5	Satisfaction with regular appraisal system	5				
6	Rotation method to attend FDP/Workshops/Seminar	5				
7	How do you rate Management for providing financial benefits to FDP/Workshop/Conference/Paper presentation	5				
8	Teaching facilities in class room		4			
9	How do you rate the accidental Insurance Policy	5				
10	Library facility in the college		4			
11	How do you rate Management for providing Transportation facility?	5				
12	Working environment of department/Institute		4			
13	Satisfaction with the Management		4			
14	Canteen facility in the campus		4			

Overall Rating = (No of Points/70\*100) = 92

  
Signature of the Faculty

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527





**Feedback on Course Curriculum from Alumni**  
**For the Academic Year 2019-2020**

5 - EXCELLENT	4 - VERY GOOD	3 - GOOD	2 - AVERAGE	1 - POOR
---------------	---------------	----------	-------------	----------

S.NO.	QUESTIONNAIRE	EXCELLENT	VERY GOOD	GOOD	AVERAGE	POOR
1	How do you rate the syllabus / curriculum prescribed for the study by the JNTUH?	5				
2	How far has the syllabus been beneficial for your higher studies and / or Professional development?		4			
3	What is the social relevance of the syllabus / curriculum on which the study program was based?	5				
4	How far did the syllabus / curriculum equip you for suitable placements in the job markets?	5				
5	How far did the curriculum raise the level of your ability to communicate effectively?	5				
6	How did the syllabus / curriculum help you in being a sensitive and responsible citizen towards social needs?	5				
7	To what extent did the curriculum is relevant to the developmental needs of the nation?		4			
8	To what extent did the experimental learning part of the curriculum is relevant to the overall development of the students, especially application of theoretical knowledge in the lab?	5				

SIGNATURE	Soumya
NAME	Ch. Soumya
HTNO	14TR1A0202
DATE	13-04-2020
BRANCH	EEE

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AKL*  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527





**Department of Electrical And Electronics Engineering I & II Sem**  
**Course Outcomes For The Academic Year 2021-22 (REGULATION - R18)**

Course Name	Course Code	Course Outcomes
<b>MATHEMATICS - I</b>	<b>MA101BS.1</b>	Write the matrix representation of a set of linear equations and to analyze the solution of the system of equations.
	<b>MA101BS.2</b>	Find the Eigen values and Eigen vectors. Reduce the quadratic form to canonical form using orthogonal transformation.
	<b>MA101BS.3</b>	Analyze the nature of sequence and series.
	<b>MA101BS.4</b>	Solve the applications on the mean value theorems. Evaluate the improper integrals using Beta and Gamma functions.
	<b>MA101BS.5</b>	Find the extreme values of functions of two variables with/without constraints.
<b>CHEMISTRY</b>	<b>CH102BS.1</b>	Understand the concepts of molecular and atomic orbital's and band theory related to conductivity.
	<b>CH102BS.2</b>	Apply different methods to convert hard water into soft water.
	<b>CH102BS.3</b>	Apply the electro chemistry concept to control corrosion process
	<b>CH102BS.4</b>	Analyze the reaction mechanism of organic molecules and synthesis of drug molecules.
	<b>CH102BS.5</b>	Apply the basic techniques of spectroscopy in medical and other fields
<b>BASIC ELECTRICAL ENGINEERING</b>	<b>EE103ES.1</b>	To analyze and solve electrical circuits using network laws and theorems.
	<b>EE103ES.2</b>	To understand and analyze basic Electric and Magnetic circuits
	<b>EE103ES.3</b>	To study the working principles of Electrical Machines
	<b>EE103ES.4</b>	To introduce components of Low Voltage Electrical Installations
	<b>EE103ES.5</b>	To analyze and solve electrical circuits using network laws and theorems.
<b>ENGINEERING WORKSHOP</b>	<b>ME105ES.1</b>	Students are practiced on machine tools and they made sample jobs
	<b>ME105ES.2</b>	All of the students are able to manufacturing of components using workshop trades including pluming, fitting, carpentry, and foundry, house wiring and welding.
	<b>ME105ES.3</b>	Students are able to Identify and apply suitable tools for different trades of Engineering processes including



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

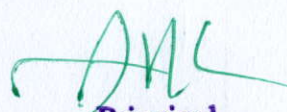
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

		drilling, material removing, measuring, chiseling.
	<b>ME105ES.4</b>	Students got basic electrical engineering knowledge by house wiring practice.
	<b>ME105ES.5</b>	Students are completed learning usage of tools and their operations with safety precautions in daily life
<b>ENGLISH</b>	<b>EN105HS.1</b>	Take part in computer – assisted multi – media language learning activities to learn individually and independently.
	<b>EN105HS.2</b>	Identify nuances of English language through audio-visual experience and group activities.
	<b>EN105HS.3</b>	Demonstrate consistent accent and intelligibility in pronunciation of English through practice
	<b>EN105HS.4</b>	Improve the fluency of students in spoken English and neutralize their mother tongue influence.
	<b>EN105HS.5</b>	Relate the use of English language appropriately for public speaking and interviews.
<b>ENGINEERING CHEMISTRY LABORATORY</b>	<b>CH106BS.1</b>	Determination of parameters like hardness of water and rate of corrosion of mild steel in various conditions.
	<b>CH106BS.2</b>	Able to perform methods such as conductometry, potentiometry and pH metry in order to find out the concentrations or equivalence points of acids and bases.
	<b>CH106BS.3</b>	Students are able to prepare polymers like bakelite and nylon-6.
	<b>CH106BS.4</b>	Estimations saponification value, surface tension and viscosity of lubricant oils.
	<b>CH106BS.5</b>	Students will learn skills related to the lubricant properties such as saponification value, surface tension and viscosity of oils.
<b>ENGLISH LANGUAGE AND COMMUNICATION SKILLS LABORATORY</b>	<b>EN107HS.1</b>	Understand the nuances of English language through audio- visual experience and group activities
	<b>EN107HS.2</b>	Students will Neutralize their accent for intelligibility
	<b>EN107HS.3</b>	Students will Speak with clarity and confidence which in turn enhances their employability skills
	<b>EN107HS.4</b>	To facilitate computer-assisted multi-media instruction enabling individualized and independent language learning
	<b>EN107HS.5</b>	To sensitize the students to the nuances of English speech sounds, word accent, intonation and rhythm
<b>DASIC ELECTRICAL ENGINEERING LABORATORY</b>	<b>EE108ES.1</b>	Verify the basic Electrical circuits through different experiments.
	<b>EE108ES.2</b>	Evaluate the performance calculations of Electrical

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

		Machines and Transformers through various testing methods.
	<b>EE108ES.3</b>	Analyze the transient responses of R, L and C circuits for different input conditions.
	<b>EE108ES.4</b>	To measure the electrical parameters for different types of DC and AC circuits using conventional and theorems approach.
	<b>EE108ES.5</b>	To study the transient response of various R, L and C circuits using different excitations.
<b>MATHEMATICS - II</b>	<b>MA201BS.1</b>	To Understand whether the given differential equation of first order is exact or not and to find solutions of linear and Bernoulli's differential equations
	<b>MA201BS.2</b>	To Analyze higher order differential equation and apply the concept of differential equation to real world problems.
	<b>MA201BS.3</b>	To Evaluate the multiple integrals and apply the concept to find areas volumes and centre of mass
	<b>MA201BS.4</b>	To Find the grad, divergence and curl of a vector, and about vector identities
	<b>MA201BS.5</b>	To Evaluate the line, surface and volume integrals and converting them from one to another
<b>APPLIED PHYSICS</b>	<b>AP202BS.1</b>	The student would be able to learn the fundamental concepts on Quantum behavior of matter in its micro state.
	<b>AP202BS.2</b>	The knowledge of fundamentals of Semiconductor physics, Optoelectronics, Lasers and fibre optics enable the students to apply to various systems like communications, solar cell, photo cells and so on.
	<b>AP202BS.3</b>	Design, characterization and study of properties of material help the students to prepare new materials for various engineering applications.
	<b>AP202BS.4</b>	Student able to find the characteristics of lasers and optical fibres.
	<b>AP202BS.5</b>	The course also helps the students to be exposed to the phenomena of electromagnetism and also to have exposure on magnetic materials and dielectric materials.
<b>PROGRAMMING FOR PROBLEM SOLVING</b>	<b>CS203ES.1</b>	To write algorithms and to draw flowcharts for solving problems
	<b>CS203ES.2</b>	To convert the algorithms/flowcharts to C programs.
	<b>CS203ES.3</b>	To code and test a given logic in the C programming language.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AK*  
Principal

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

	<b>CS203ES.4</b>	To decompose a problem into functions and to develop modular reusable code
	<b>CS203ES.5</b>	To use arrays, pointers, strings and structures to write C programs.
<b>ENGINEERING GRAPHICS</b>	<b>ME204ES.1</b>	Apply computer aided drafting tools to create 2D and 3D objects
	<b>ME204ES.2</b>	Sketch conics and different types of solids
	<b>ME204ES.3</b>	Appreciate the need of Sectional views of solids.
	<b>ME204ES.4</b>	Development of surfaces of solids
	<b>ME204ES.5</b>	Read and interpret engineering drawings
<b>APPLIED PHYSICS LABORATORY</b>	<b>AP205BS.1</b>	Know the determination of the Planck's constant using Photo electric effect and identify the material whether it is n-type or p-type by Hall experiment.
	<b>AP205BS.2</b>	Appreciate quantum physics in semiconductor devices and optoelectronics.
	<b>AP205BS.3</b>	Gain the knowledge of applications of dielectric constant.
	<b>AP205BS.4</b>	Understand the variation of magnetic field and behavior of hysteresis curve.
	<b>AP205BS.5</b>	Carried out data analysis.
<b>PROGRAMMING FOR PROBLEM SOLVING LABORATORY</b>	<b>CS206ES.1</b>	Formulate the algorithms for simple problems
	<b>CS206ES.2</b>	Translate given algorithms to a working and correct program
	<b>CS206ES.3</b>	Identify and correct logical errors encountered during execution
	<b>CS206ES.4</b>	Correct syntax errors as reported by the compilers
	<b>CS206ES.5</b>	Represent and manipulate data with arrays, strings and structures
<b>ENVIRONMENTAL SCIENCE</b>	<b>*MC209ES.1</b>	Understand /evaluate / develop technologies on the basis of ecological principles and environmental regulations which in turn helps in sustainable development
	<b>*MC209ES.2</b>	Understanding the importance of ecological balance for sustainable development.
	<b>*MC209ES.3</b>	Understanding the impacts of developmental activities and mitigation measures.
	<b>*MC209ES.4</b>	Understanding the environmental policies and regulations
	<b>*MC209ES.5</b>	Understanding the Environmental Pollution and Control Technologies
<b>ENGINE</b>	<b>EE301ES.1</b>	Determine resultant of forces acting on a body and

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

<b>ERING MECHANICS</b>		analyze equilibrium of a body subjected to a system of forces.
	<b>EE301ES.2</b>	Solve problem of bodies subjected to friction.
	<b>EE301ES.3</b>	Find the location of centroid and calculate moment of inertia of a given section.
	<b>EE301ES.4</b>	Understand the kinetics and kinematics of a body undergoing rectilinear, curvilinear, rotatory motion and rigid body motion.
	<b>EE301ES.5</b>	Solve problems using work energy equations for translation, fixed axis rotation and plane motion and solve problems of vibration.
<b>ELECTRICAL CIRCUIT ANALYSIS</b>	<b>EE302PC.1</b>	Apply network theorems for the analysis of electrical circuits.
	<b>EE302PC.2</b>	Obtain the transient and steady-state response of electrical circuits.
	<b>EE302PC.3</b>	Analyze circuits in the sinusoidal steady-state (single-phase and three-phase).
	<b>EE302PC.4</b>	Analyze Transfer functions/Network functions in Electrical Circuit.
	<b>EE302PC.5</b>	Analyze two port circuit behavior.
<b>ANALOG ELECTRONICS</b>	<b>EE303PC.1</b>	Know the characteristics, utilization of various components.
	<b>EE303PC.2</b>	Understand the biasing techniques.
	<b>EE303PC.3</b>	Design and analyze various rectifiers, small signal amplifier circuits.
	<b>EE303PC.4</b>	Design sinusoidal and non-sinusoidal oscillators.
	<b>EE303PC.5</b>	A thorough understanding, functioning of OP-AMP, design OP-AMP based circuits with linear integrated circuits.
<b>ELECTRICAL MACHINES – I</b>	<b>EE304PC.1</b>	To Identify different parts of a DC generators & understand its operation, different excitation and starting methods of DC Generators.
	<b>EE304PC.2</b>	To Identify different parts of a DC Motors & understand its operation, different excitation and starting methods of DC Motors.
	<b>EE304PC.3</b>	To Carry out different testing methods to predetermine the efficiency of DC machines and control the voltage and speed of a DC machines.
	<b>EE304PC.4</b>	To Analyze single phase and three phase transformers circuits.
	<b>EE304PC.5</b>	To Carry out different testing methods to predetermine the efficiency of transformers.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AAL*

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

<b>ELECTROMAGNETIC FIELDS</b>	<b>EE305PC.1</b>	To understand the basic laws of electromagnetism.
	<b>EE305PC.2</b>	To obtain the electric and magnetic fields for simple configurations under static conditions.
	<b>EE305PC.3</b>	To analyze time varying electric and magnetic fields.
	<b>EE305PC.4</b>	To understand Maxwell's equation in different forms and different media.
	<b>EE305PC.5</b>	To understand the propagation of EM waves.
<b>ELECTRICAL MACHINES LAB – I</b>	<b>EE306PC.1</b>	Start and control the Different DC Machines
	<b>EE306PC.2</b>	Assess the performance of different machines using different testing methods
	<b>EE306PC.3</b>	Identify different conditions required to be satisfied for self - excitation of DC Generators
	<b>EE306PC.4</b>	Separate iron losses of DC machines into different components
	<b>EE306PC.5</b>	Identify different performance characteristics
<b>ANALOG ELECTRONICS LAB</b>	<b>EE307PC.1</b>	Know the characteristics, utilization of various components.
	<b>EE307PC.2</b>	Understand the biasing techniques
	<b>EE307PC.3</b>	Design and analyze various rectifiers, small signal amplifier circuits
	<b>EE307PC.4</b>	Design sinusoidal and non-sinusoidal oscillators.
	<b>EE307PC.5</b>	A thorough understanding, functioning of OP-AMP, design OP-AMP based circuits with linear integrated circuits
<b>ELECTRICAL CIRCUITS LAB</b>	<b>EE308PC.1</b>	Analyze complex DC and AC linear circuits
	<b>EE308PC.2</b>	Apply concepts of electrical circuits across engineering
	<b>EE308PC.3</b>	Evaluate response in a given network by using theorems
	<b>EE308PC.4</b>	Analyze a given network by applying various Network Theorems
	<b>EE308PC.5</b>	Measure three phase Active and Reactive power
<b>GENDER SENSITIZATION LAB</b>	<b>*MC309.1</b>	Students will have developed a better understanding of important issues related to gender in contemporary India.
	<b>*MC309.2</b>	Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature and film.
	<b>*MC309.3</b>	Students will attain a finer grasp of how gender discrimination works in our society and how to counter it.
	<b>*MC309.4</b>	Students will acquire insight into the gendered division of

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*DAC*  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

		labour and its relation to politics and economics.
	<b>*MC309.5</b>	Men and women students and professionals will be better equipped to work and live together as equals.
<b>LAPLACE TRANSFORMS, NUMERICAL METHODS AND COMPLEX VARIABLES</b>	<b>MA401BS.1</b>	To understand the Standard functions of Laplace transforms and inverse Laplace transforms.
	<b>MA401BS.2</b>	To obtain and estimate the value for the given data using interpolation.
	<b>MA401BS.3</b>	To analyze and find the numerical solutions for a given first order ODE's
	<b>MA401BS.4</b>	To understand differentiation and integration of complex valued functions..
	<b>MA401BS.5</b>	To analyze the complex function with reference to their analyticity, integration using Cauchy's integral and residue theorems.
<b>ELECTRICAL MACHINES – II</b>	<b>EE402PC.1</b>	To Understand the concepts of rotating magnetic fields.
	<b>EE402PC.2</b>	Analyze performance characteristics of ac machines.
	<b>EE402PC.3</b>	To analyze the concept of regulation and its calculations.
	<b>EE402PC.4</b>	To Understand the operation of ac machines.
	<b>EE402PC.5</b>	To understand operation, construction and types of single-phase motors.
<b>DIGITAL ELECTRONICS</b>	<b>EE403PC.1</b>	Understand working of logic families and logic gates.
	<b>EE403PC.2</b>	Design and implement Combinational circuits.
	<b>EE403PC.3</b>	Design and implement Sequential logic circuits.
	<b>EE403PC.4</b>	Understand the process of Analog to Digital conversion and Digital to Analog conversion.
	<b>EE403PC.5</b>	Be able to use PLDs to implement the given logical problem.
<b>CONTROL SYSTEMS</b>	<b>EE404PC.1</b>	To Understand the modeling of linear-time-invariant systems using transfer function.
	<b>EE404PC.2</b>	To Understand the concept of stability and its assessment for linear-time invariant systems using Time Domain Analysis.
	<b>EE404PC.3</b>	To Understand the concept of stability and its assessment for linear-time invariant systems using Frequency Domain Analysis.
	<b>EE404PC.4</b>	To Understand how to Design simple feedback controllers.
	<b>EE404PC.5</b>	To Understand the modeling of linear-time-invariant systems using state space representation.
<b>POWER SYSTEM – I</b>	<b>EE405PC.1</b>	To Understand the concepts of power systems and renewable sources of electrical power.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AKL*

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

	<b>EE405PC.2</b>	To Evaluate the power tariff methods.
	<b>EE405PC.3</b>	To Understand the Insulators and underground cables.
	<b>EE405PC.4</b>	To Determine the electrical circuit parameters of transmission lines and corona.
	<b>EE405PC.5</b>	To Understand A.C. and D.C. distribution systems.
<b>DIGITAL ELECTRONICS LAB</b>	<b>EE406PC.1</b>	Understand working of logic families and logic gates
	<b>EE406PC.2</b>	Design and implement Combinational and Sequential logic circuits
	<b>EE406PC.3</b>	Understand the process of Analog to Digital conversion and Digital to Analog conversion
	<b>EE406PC.4</b>	Be able to use PLDs to implement the given logical problem
	<b>EE406PC.5</b>	Implement synchronous state machines using flip-flops
<b>ELECTRICAL MACHINES LAB – II</b>	<b>EE407PC.1</b>	Assess the performance of different machines using different testing methods
	<b>EE407PC.2</b>	To convert the Phase from three phase to two phase and vice versa
	<b>EE407PC.3</b>	Compensate the changes in terminal voltages of synchronous generator after estimating the change by different methods
	<b>EE407PC.4</b>	Control the active and reactive power flows in synchronous machines
	<b>EE407PC.5</b>	Start different machines and control the speed and power factor
<b>CONTROL SYSTEMS LAB</b>	<b>EE408PC.1</b>	How to improve the system performance by selecting a suitable controller and/or a compensator for a specific application
	<b>EE408PC.2</b>	Apply various time domain and frequency domain techniques to assess the system performance
	<b>EE408PC.3</b>	Apply various control strategies to different applications (example: Power systems, electrical drives etc)
	<b>EE408PC.4</b>	Test system controllability and observability using state space representation and applications of state space representation to various systems
	<b>EE408PC.5</b>	Design various controllers and compensators to improve system performance
<b>CONSTITUTION OF INDIA</b>	<b>*MC409.1</b>	Meaning of the constitution law and constitutionalism
	<b>*MC409.2</b>	Historical perspective of the Constitution of India
	<b>*MC409.3</b>	Salient features and characteristics of the Constitution of India
	<b>*MC409.4</b>	Scheme of the fundamental rights
	<b>*MC409.5</b>	The scheme of the Fundamental Duties and its legal

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*(Handwritten Signature)*

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

		status
<b>POWER ELECTRONICS</b>	<b>EE501PE.1</b>	To understand the differences between signal level and power level devices.
	<b>EE501PE.2</b>	To Analyze controlled rectifier circuits.
	<b>EE501PE.3</b>	To Analyze the operation of DC-DC choppers.
	<b>EE501PE.4</b>	To analyze the operation of voltage source inverters.
	<b>EE501PE.5</b>	To analyze the operation of AC Voltage regulator and cyclo-converter.
<b>POWER SYSTEM – II</b>	<b>EE502PE.1</b>	Analyze transmission line performance.
	<b>EE502PE.2</b>	Apply load compensation techniques to control reactive power.
	<b>EE502PE.3</b>	Understand the application of per unit quantities.
	<b>EE502PE.4</b>	Design over voltage protection and insulation coordination.
	<b>EE502PE.5</b>	Determine the fault currents for symmetrical and unbalanced faults.
<b>MEASUREMENTS AND INSTRUMENTATION</b>	<b>EE503PE.1</b>	To understand different types of measuring instruments.
	<b>EE503PE.2</b>	To analyze their construction of potentiometers and instrument transformers.
	<b>EE503PE.3</b>	To apply to measure of energy and power.
	<b>EE503PE.4</b>	Identify the measuring instruments of resistance, inductance and capacitance.
	<b>EE503PE.5</b>	Apply the knowledge about transducers.
<b>HIGH VOLTAGE ENGINEERING</b>	<b>EE512PE.1</b>	To understand the basic laws of electromagnetism.
	<b>EE512PE.2</b>	To obtain the electric and magnetic fields for simple configurations under static conditions.
	<b>EE512PE.3</b>	To analyze time varying electric and magnetic fields
	<b>EE512PE.4</b>	To understand Maxwell's equation in different forms and different media.
	<b>EE512PE.5</b>	To understand the propagation of EM waves.
<b>BUSINESS ECONOMICS AND FINANCIAL ANALYSIS</b>	<b>SM504MS.1</b>	The students will understand the various Forms of Business and the impact of economic variables on the Business.
	<b>SM504MS.2</b>	The Demand, Supply, Production, Cost, Market Structure, Pricing aspects are learnt
	<b>SM504MS.3</b>	The Students can study the firm's financial position by analyzing the Financial Statements of a Company.
	<b>SM504MS.4</b>	Ascertain the provisions of capital
	<b>SM504MS.5</b>	Enumerate the concept of capital budgeting and allocations of the resources through capital
<b>POWER SYSTEM</b>	<b>EE505PC.1</b>	Perform various transmission line calculations

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

<b>SIMULATION LAB</b>	<b>EE505PC.2</b>	Understand Different circuits time constants
	<b>EE505PC.3</b>	Analyze the experimental data and draw the conclusions
	<b>EE505PC.4</b>	To perform voltage distributions across insulator strings
	<b>EE505PC.5</b>	To understand the high frequency transients
<b>POWER ELECTRONICS LAB</b>	<b>EE506PC.1</b>	Understand the operating principles of various power electronic converters
	<b>EE506PC.2</b>	Use power electronic simulation packages & hardware to develop the power converters.
	<b>EE506PC.3</b>	Analyze and choose the appropriate converters for various applications
	<b>EE506PC.4</b>	Apply the concepts of power electronic converters for efficient conversion/control of power from source to load.
	<b>EE506PC.5</b>	Design the power converter with suitable switches meeting a specific load requirement.
<b>MEASUREMENTS AND INSTRUMENTATION LAB</b>	<b>EE507PC.1</b>	Student is able to choose instruments
	<b>EE507PC.2</b>	Student is able to test any instrument
	<b>EE507PC.3</b>	Student is able to find the accuracy of any instrument by performing experiment
	<b>EE507PC.4</b>	Student is able to calibrate PMMC instrument using D.C potentiometer
	<b>EE507PC.5</b>	Student is able to calibrate LPF Watt Meter, energy meter, P. F Meter using electro dynamo meter type instrument as the standard instrument
<b>ADVANCED COMMUNICATION SKILLS LAB</b>	<b>EN508HS.1</b>	To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts
	<b>EN508HS.2</b>	Further, they would be required to communicate their ideas relevantly and coherently in writing.
	<b>EN508HS.3</b>	To prepare all the students for their placements
	<b>EN508HS.4</b>	Gathering ideas and information to organize ideas relevantly and coherently.
	<b>EN508HS.5</b>	Transferring information from non-verbal to verbal texts and vice-versa.
<b>INTELLECTUAL PROPERTY RIGHTS</b>	<b>*MC510.1</b>	Introduction to Intellectual property
	<b>*MC510.2</b>	Trade Marks
	<b>*MC510.3</b>	Law of copy rights
	<b>*MC510.4</b>	Trade Secrets
	<b>*MC510.5</b>	New development of intellectual property
<b>NON-CONVENTIONAL</b>	<b>ME800OE.1</b>	Understand the basic concepts and operation of

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

<b>SOURCES OF ENERGY</b>		renewable energy systems
	<b>ME800OE.2</b>	Remember the ideas and statistics of current RES availability and usage.
	<b>ME800OE.3</b>	Analyze the problems in RES installation in real time.
	<b>ME800OE.4</b>	Identify the other NCES and available sources improvement.
	<b>ME800OE.5</b>	Apply the renewable energy systems in real time applications.
<b>POWER SEMICONDUCTOR DRIVES</b>	<b>EE612PE.1</b>	To Identify the drawbacks of speed control of motor by conventional methods AND Differentiate Phase controlled and chopper-controlled DC drives speed-torque characteristics merits and demerits.
	<b>EE612PE.2</b>	To chopper-controlled DC drives speed-torque characteristics merits and demerits.
	<b>EE612PE.3</b>	Understand Control of Induction Motor drive speed-torque characteristics using different control strategies its merits and demerits.
	<b>EE612PE.4</b>	To Describe Slip power recovery schemes.
	<b>EE612PE.5</b>	Understand Control of Synchronous Motors speed-torque characteristics using different control strategies its merits and demerits.
<b>SIGNALS AND SYSTEMS</b>	<b>EE601PC.1</b>	Understand the basic concept of signals and systems , analogy between vectors and signals.
	<b>EE601PC.2</b>	Understand the fourier series, fourier transform an its properties.
	<b>EE601PC.3</b>	Understand the relation between linear system and bandwidth.
	<b>EE601PC.4</b>	Analyze the properties of laplace transform and z transform.
	<b>EE601PC.5</b>	Understand the concept of sampling theorem and properties of correlation.
<b>MICROPROCESSORS &amp; MICROCONTROLLERS</b>	<b>EE602PC.1</b>	Understand the internal architecture, organization and assembly language programming of 8086 processors.
	<b>EE602PC.2</b>	Understand the internal architecture, organization and assembly language programming of 8051/controllers.
	<b>EE602PC.3</b>	Understand the interfacing techniques to 8086 and 8051 based systems.
	<b>EE602PC.4</b>	Understands the internal architecture of ARM processors.
	<b>EE602PC.5</b>	Understand the basic concepts of advanced ARM processors.
<b>POWER SYSTEM PROTECTION</b>	<b>EE603PC.1</b>	Compare and contrast electromagnetic, static and microprocessor-based Relays.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*(Signature)*

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR



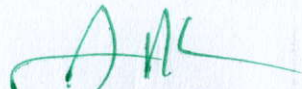
**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

	<b>EE603PC.2</b>	Apply technology to protect power system components.
	<b>EE603PC.3</b>	Select relay settings of neutral grounding for overall protection.
	<b>EE603PC.4</b>	To understand the Amplitude and Phase comparators.
	<b>EE603PC.5</b>	Analyze quenching mechanisms used in air, oil and vacuum circuit breakers.
<b>POWER SYSTEM OPERATION AND CONTROL</b>	<b>EE604PC.1</b>	Analyze Load Flow Studies
	<b>EE604PC.2</b>	Understand economic operation of power systems.
	<b>EE604PC.3</b>	Address load frequency control problem.
	<b>EE604PC.4</b>	Analyze whether the machine is in stable or unstable position.
	<b>EE604PC.5</b>	Analyze various functions of Energy Management System (EMS) functions.
<b>POWER SYSTEM LAB</b>	<b>EE605PC.1</b>	Perform various load flow techniques
	<b>EE605PC.2</b>	Understand Different protection methods
	<b>EE605PC.3</b>	Analyze the experimental data and draw the conclusions.
	<b>EE605PC.4</b>	Perform testing of CT, PT's and Insulator strings
	<b>EE605PC.5</b>	Perform fault analysis on Transmission line models and Generators.
<b>MICROPROCESSORS &amp; MICROCONTROLLERS LAB</b>	<b>EE606PC.1</b>	Arithmetic, Logical, String Operations on 16 Bit and 32-Bit Data.
	<b>EE606PC.2</b>	Time delay Generation Using Timers of 8051.
	<b>EE606PC.3</b>	Serial Communication from / to 8051 to / from I/O devices.
	<b>EE606PC.4</b>	7 Segment Display to 8051.
	<b>EE606PC.5</b>	Sequence Generator Using Serial Interface in 8051.
<b>SIGNALS AND SYSTEMS LAB</b>	<b>EE607PC.1</b>	Understand the concepts of continuous time and discrete time systems.
	<b>EE607PC.2</b>	Analyze systems in complex frequency domain.
	<b>EE607PC.3</b>	Understand sampling theorem and its implications.
	<b>EE607PC.4</b>	Develop ability to analyze linear systems and signals
	<b>EE607PC.5</b>	Develop critical understanding of mathematical methods to analyze linear systems and signals
<b>ENVIRONMENTAL SCIENCE</b>	<b>*MC609.1</b>	Understanding the importance of ecological balance for sustainable development.
	<b>*MC609.2</b>	Understanding the impacts of developmental activities and mitigation measures
	<b>*MC609.3</b>	Understanding the environmental policies and regulations
	<b>*MC609.4</b>	Understand /evaluate / develop technologies on the basis of ecological principles

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**

Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

	<b>*MC609.5</b>	Understand /evaluate / develop technologies on environmental regulations which in turn helps in sustainable development
<b>PRINCIPLES OF ENTREPRENEURSHIP</b>	<b>MT701OE.1</b>	Introduction to Entrepreneurship.
	<b>MT701OE.2</b>	Financing and Managing.
	<b>MT701OE.3</b>	Industrial Financial Support.
	<b>MT701OE.4</b>	Production and marketing management.
	<b>MT701OE.5</b>	Labour legislation.
<b>DIGITAL CONTROL SYSTEMS</b>	<b>EE711PE.1</b>	Obtain discrete representation of LTI systems.
	<b>EE711PE.2</b>	Analyze stability of open loop and closed loop discrete-time systems.
	<b>EE711PE.3</b>	Obtain State space models of discrete systems
	<b>EE711PE.4</b>	Design and analyze digital controllers.
	<b>EE711PE.5</b>	Design state feedback and output feedback controllers
<b>INDUSTRIAL ELECTRICAL SYSTEMS</b>	<b>EE723PE.1</b>	To understand electrical system components representing the systems with standard symbols and drawings, SLD.
	<b>EE723PE.2</b>	To understand the electrical wiring systems for residential, commercial and industrial consumers.
	<b>EE723PE.3</b>	To understand illumination systems.
	<b>EE723PE.4</b>	To understand various components of industrial electrical systems.
	<b>EE723PE.5</b>	To analyze and select the proper size of various electrical system components.
<b>FUNDAMENTALS OF MANAGEMENT FOR ENGINEERS</b>	<b>SM701MS.1</b>	The students understand different concepts of management in all aspects.
	<b>SM701MS.2</b>	To know the scope and importance of leadership qualities and skills.
	<b>SM701MS.3</b>	Student understand the importance of Motivation, Power, Authority flow in the organization.
	<b>SM701MS.4</b>	To explore student in all the studies of Management and theories of Management.
	<b>SM701MS.5</b>	Students learn the importance of planning and organizing theories in depth.
<b>ELECTRICAL &amp; ELECTRONICS DESIGN IAR</b>	<b>EE701PC.1</b>	Get practical knowledge related to electrical
	<b>EE701PC.2</b>	Fabricate basic electrical circuit elements/networks
	<b>EE701PC.3</b>	Trouble shoot the electrical circuits
	<b>EE701PC.4</b>	Design filter circuit for application
	<b>EE701PC.5</b>	Get hardware skills such as soldering, winding etc.
<b>DATABASE MANAGEMENT SYSTEMS</b>	<b>CS404PC.1</b>	To Understand model E-R diagrams for enterprise database.

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

**Principal**

Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S.)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
 EDUCATIONAL INSTITUTIONS

	<b>CS404PC.2</b>	To formulate queries using SQL.
	<b>CS404PC.3</b>	To apply different normal forms to design the database.
	<b>CS404PC.4</b>	To summarize concurrency control and recovery algorithms.
	<b>CS404PC.5</b>	To identify suitable indices and hashing mechanisms for effective storage.
<b>AI TECHNIQUES IN ELECTRICAL ENGINEERING</b>	<b>EE813PE.1</b>	Understand feed forward neural networks, feedback neural networks and learning techniques.
	<b>EE813PE.2</b>	Understand fuzziness involved in various systems and fuzzy set theory.
	<b>EE813PE.3</b>	Develop fuzzy logic control for applications in electrical engineering.
	<b>EE813PE.4</b>	Develop genetic algorithm for applications in electrical engineering.
	<b>EE813PE.5</b>	Applications of AI Techniques.
<b>ELECTRICAL DISTRIBUTION SYSTEMS</b>	<b>EE822PE.1</b>	Distinguish between transmission, and distribution line and design the feeders
	<b>EE822PE.2</b>	Compute power loss and voltage drop of the feeders
	<b>EE822PE.3</b>	Design protection of distribution systems
	<b>EE822PE.4</b>	Understand the importance of power factor improvement
	<b>EE822PE.5</b>	Understand the importance of voltage control

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)





**PROGRAM LEVEL CO-PO MAPPING**  
**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

S.NO	SUBJECT NAME	SUBJECT CODE	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1	Mathematics - I	MA101BS	2.5	2.67	2.5									2.5	2	2.5	
2	Chemistry	CH102BS	2.5	2.17	2.5	2		2			2.5		2.5	2.5	2.4	2.5	2.5
3	Basic Electrical Engineering	EE103ES	2.2	2	2.5			2			2.5		2.5	2.5	2.4	2.5	2.5
4	Engineering Workshop	ME105ES	2.8	2				2	2			3	2	2	2.5	2	
5	English	EN105HS				3						3			2	2.2	
6	Engineering Chemistry Lab	CH106BS	2	1.83	2			2	2					2		2	2
7	English Language and Communication Skills Lab	EN107HS									3	3					
8	Basic Electrical Engineering Lab	EE108ES	3	2.67	2.5	2					2			2	2.6	2.33	2.5
9	Mathematics-II	MA201BS	2.67	2.5	2.5										2	2.5	
10	Applied Physics	AP202BS	3	2											2.25	2	
11	Programming for Problem Solving	CS203ES	3	3	2.17	2.2	2.5				3	3	3	3		2	
12	Engineering Graphics	ME204ES	2.4	2.33			3	3		2.33		2.5		2		2	
13	Applied Physics Lab	AP205BS	3	2	2	2						2			2.25	2	
14	Programming for Problem Solving Lab	CS206ES	3	3	3	2	2.33			2	2	2	2.17	2		2	
15	Environmental Science	*MC209ES			2.17		3	2	3	2				2			
16	Engineering Mechanics	EE301ES	2.83	2.83		2								2			
17	Electrical Circuit Analysis	EE302PC	2.5	2.5	2	2.6		2			2.33		2		2.33	2.33	2.33
18	Analog Electronics	EE303PC	2.25	2.25	2.25	2.25	2.25					2.25				2.25	
19	Electrical Machines-I	EE304PC	2.4	2.25	2.5	2.33	2.5	2			2.2		2	2	2.33	2.17	2.5
20	Electromagnetic Fields	EE305PC	2.75	2.33	2	2								2	3	3	
21	Electrical Machines lab-I	EE306PC	2.5	2.67	2.5	3	2				2.5				2.5	2.75	
22	Analog Electronics Lab	EE307PC	3	3	3	3	3					3				3	
23	Electrical Circuits Lab	EE308PC	2.2	2.5	2.2	2.5	2.33	2			2.67		2		2.17	3	2.17
24	Gender Sensitization Lab	*MC309						3		2.25							
25	Laplace Transforms,	MA401BS	3	2.83	2.83	2.33								2	2	2.5	

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

*AVL*  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

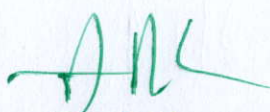
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

	Numerical Methods & Complex variables																
26	Electrical Machines – II	EE402PC	3	2.67	2.67	2.5	2.67	2.33			2		2	2.75	2.5	2	2.67
27	Digital Electronics	EE403PC	2.5	2.5	2	2.6		2			2.33		2		2.33	2.33	2.33
28	Control Systems	EE404PC	2.67	2.6	2.4	3	2.5	2.33		3	3		3	3	3	2	2.33
29	Power System - I	EE405PC	2.67	2.25	2.67	2.33	2.17	2	2	2	2	2.67	3	2.5	2.67	2	2.67
30	Digital Electronics Lab	EE406PC	2.67	2.67	2.2	2.8		2			2.67		2		2.5	2.5	2.33
31	Electrical Machines Lab - II	EE407PC	2.6	2.33	2.25	2.33					3			2.5	2		2
32	Control Systems Lab	EE408PC	2.25	3	2.67	2	2.67	2					3	2.17	2.33	2.33	
33	Constitution of India	*MC409			2	2									1.4	1.33	1.2
34	Power Electronics	EE501PE	2.33	2	2.33	2.33	2.5				2	2.33	2	2.33	2.67	2.5	
35	Power System-II	EE502PE	3	2.5	2.5	3	3	2.5	3		2	2.5	2.33	2.5	2.5	2.5	3
36	Measurements and Instrumentation	EE503PE	2.67	2.67	2	3	2				2		2.4	2.25	2.5	2	3
37	High Voltage Engineering	EE512PE	1.8	1	2.8												
38	Business Economics and Financial Analysis	SM504 MS									2	2	2.8	3			2.5
39	Power System Simulation Lab	EE505PC	2	2.17	2.17	2	2.33	2		2	2	2	2	1.75	2.5	2.33	2
40	Power Electronics Lab	EE506PC	2.17	2.17	2.33	2.5	2.25					2.33	2.5	2.17	2.17	2.67	2
41	Measurements and Instrumentation Lab	EE507PC	2	2.5	2		3								2.5	2	
42	Advanced Communication Skills Lab	EN508HS									3	3		2			
43	Intellectual Property Rights	*MC510			2					2				2			
44	Non-Conventional Sources Of Energy	ME8000E	2.2	2.2	1.8	0.8	1.4	1.4								0.2	0.6
45	Power Semiconductor Drives	EE612PE	2.33	2.33	2.25	2	2						2	2.5	2.33	2.17	2
46	Signals And Systems	EE601PC	2.83	2.33	2.2	2.33	2.67								2	2.67	2
47	Microprocessors & Microcontrollers	EE602PC	2.17	2.25	2.5	3	2.5	2							2.67	2.75	2
48	Power System Protection	EE603PC	3	2.2	2.33	2.17	2.5	3			2.67	3	2	2.5	2.5	3	3
49	Power System Operation and Control	EE604PC	2.2	1.5	1.2	1.88	2.6	2			2	3	2	2.5	2.5	2.5	2.33
50	Power System Lab	EE605PC	3	3	3	3	2						2	2.75	2	2.83	
51	Microprocessors & Microcontrollers Lab	EE606PC	2.2	2.2	2.33	2					2.33		2.5		2.5	2.33	1.67
52	Signals and	EE607PC	1.83	2.2	2.33	2	3				1	1.67		3	2	2.67	2

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 KARIMNAGAR



# SREE CHAITANYA INSTITUTE OF TECHNOLOGICAL SCIENCES

(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)




**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

	Systems Lab																	
53	Environmental Science	*MC609			2.17		3	2	3	2				2				
54	Principles Of Entrepreneurship	MT7010E								1	2	2.33	2.4	2.5				
55	Digital Control systems	EE711PE	2.33	2	2.2	1.8	2		2		2		2.33		2.2	2.33	1.66	
56	Industrial Electrical Systems	EE723PE	2.2	1.5	2.2	2.33	2.6	2			2	3	2	2.5	2.5	2.5	2.33	
57	Fundamentals of Management for Engineers	SM701MS									2	2.33	2.5	2.4				
58	Electrical & Electronics Design Lab	EE701PC	2	2.67	3				2.5	2		2	2	1	2.5	2.5	3	2.33
59	Industrial Oriented Mini Project/ Summer Internship	EE702PC	2	2.5	3	2.5	2				3			3	2	2	2	
60	Seminar	EE703PC	2	3							2.5	3		3	2	2	2	
61	Project Stage - I	EE704PC	3	3	3	3	3	3	1	3	3	3	3	3	2	2.5	2	
62	Database Management Systems	CS404PC	3	3	3	3	3	2	2	2.33	2	1.83	2.5	1.5				
63	AI Techniques In Electrical Engineering	EE813PE	1.66	2		2.66			1		1.66	2		2.33		2	1.33	
64	Electrical Distribution Systems	EE822PE	2.67	2.5	2.6	2.33	3	2.6		2			2	1.5	2.83	2	1.33	
65	Project Stage - II	EE801PC	3	3	3	3	3	3	1	3	3	3	3	3	2	2.5	2	
Count Relevance			55	54	54	46	35	31	13	17	36	30	33	44	45	53	40	
SUM			135.4	129.5	123.22	105.85	88.27	65.66	23	35.57	81.27	71.41	73.76	103.65	101.68	115.78	83.57	
Direct Attainment			2.46	2.4	2.28	2.3	2.52	2.12	1.77	2.1	2.26	2.38	2.24	2.36	2.26	2.184	2.09	
Indirect Attainment			2.32	2.28	2.15	2.2	2.41	2.51	2.1	2.24	2.52	2.21	2.53	2.62	2.34	2.35	2.24	
Overall Attainment			2.43	2.37	2.29	2.3	2.5	2.18	1.9	2.12	2.32	2.34	2.32	2.42	2.28	2.2	2.18	

COURSE	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
DIRECT ATTAINMENT (AVERAGE)	2.46	2.4	2.28	2.3	2.52	2.12	1.77	2.1	2.26	2.38	2.24	2.36
DIRECT ATTAINMENT (80% AVERAGE)	1.968	1.92	1.824	1.84	2.016	1.696	1.418	1.68	1.808	1.904	1.792	1.888
INDIRECT ATTAINMENT (AVERAGE)	2.32	2.28	2.15	2.2	2.41	2.51	2.12	2.24	2.52	2.21	2.53	2.62
INDIRECT ATTAINMENT (20% AVERAGE)	0.464	0.456	0.43	0.44	0.482	0.502	0.424	0.448	0.504	0.442	0.506	0.524
OVERALL PO	2.432	2.376	2.254	2.28	2.498	2.198	1.84	2.128	2.312	2.346	2.298	2.412

LMD COLONY, THIMMAPUR, KARIMNAGAR, T.S. - 505527

  
**Principal**  
 Sree Chaitanya Institute of Technological Sciences  
 L.M.D. Colony, KARIMNAGAR (T.S.)



**SREE CHAITANYA**  
**INSTITUTE OF TECHNOLOGICAL SCIENCES**  
(Approved by AICTE, New Delhi, Affiliated to J.N.T.U.H., Hyderabad)



**SREE CHAITANYA**  
EDUCATIONAL INSTITUTIONS

<b>ATTAINMENT</b> <b>(80%+20%)</b>												
---------------------------------------	--	--	--	--	--	--	--	--	--	--	--	--

**Principal**  
Sree Chaitanya Institute of Technological Sciences  
L.M.D. Colony, KARIMNAGAR (T.S)